

RoHS Compliant Product
A suffix of "-C" specifies halogen & lead-free

DESCRIPTION

The SSG13N10S-C is the highest performance N-ch MOSFETs with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications .

The SSG13N10S-C meet the RoHS and Green Product requirement with full function reliability approved.

FEATURES

- High Speed Power Switching
- Super Low Gate Charge
- Green Device Available

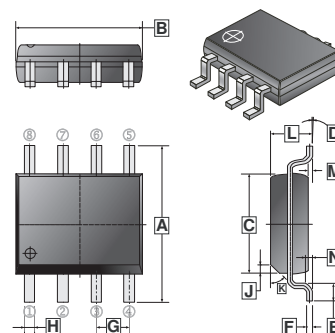
PACKAGE INFORMATION

| Package | MPQ | Leader Size |
|---------|------|-------------|
| SOP-8 | 2.5K | 13 inch |

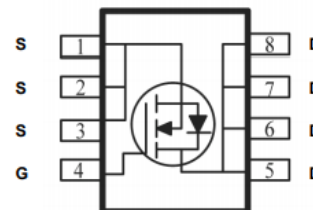
ORDER INFORMATION

| Part Number | Type |
|-------------|---------------------------------|
| SSG13N10S-C | Lead (Pb)-free and Halogen-free |

SOP-8



| REF. | Millimeter | | REF. | Millimeter | |
|------|------------|------|------|------------|-------|
| | Min. | Max. | | Min. | Max. |
| A | 5.79 | 6.20 | H | 0.33 | 0.51 |
| B | 4.70 | 5.11 | J | 0.375 | REF. |
| C | 3.80 | 4.00 | K | 45° | REF. |
| D | 0° | 8° | L | 1.3 | 1.752 |
| E | 0.40 | 1.27 | M | 0 | 0.25 |
| F | 0.10 | 0.25 | N | 0.25 | REF. |
| G | 1.27 | TYP. | | | |



ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$ unless otherwise specified)

| Parameter | Symbol | Rating | Unit | |
|---|------------------------|-------------------------|--------------------|---|
| Drain-Source Voltage | V_{DS} | 100 | V | |
| Gate-Source Voltage | V_{GS} | ± 20 | V | |
| Continuous Drain Current ¹ @ $V_{GS}=10\text{V}$ | $T_A=25^\circ\text{C}$ | 13 | A | |
| | $T_A=70^\circ\text{C}$ | 10 | | |
| Pulsed Drain Current ² | I_{DM} | 55 | A | |
| Power Dissipation ³ | $T_A=25^\circ\text{C}$ | P_D | 3.1 | W |
| Operating Junction & Storage Temperature Range | T_J, T_{STG} | -55~150 | $^\circ\text{C}$ | |
| Thermal Data | | | | |
| Thermal Resistance Junction-Ambient ¹ | $R_{\theta JA}$ | $t \leq 10\text{s}, 40$ | $^\circ\text{C/W}$ | |
| | | Steady State ,75 | | |
| Thermal Resistance Junction-Case ¹ | $R_{\theta JC}$ | 24 | | |

ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$ unless otherwise specified)

| Parameter | Symbol | Min. | Typ. | Max. | Unit | Test condition |
|---|--------------|------|------|-----------|------------|--|
| Drain-Source Breakdown Voltage | BV_{DSS} | 100 | - | - | V | $V_{GS}=0V, I_D=250\mu A$ |
| Gate-Threshold Voltage | $V_{GS(th)}$ | 1 | - | 2.5 | V | $V_{DS}=V_{GS}, I_D=250\mu A$ |
| Gate-Source Leakage Current | I_{GSS} | - | - | ± 100 | nA | $V_{GS}=\pm 20V$ |
| Drain-Source Leakage Current | I_{DSS} | - | - | 1 | μA | $V_{DS}=80V, V_{GS}=0, T_J=25^\circ C$ |
| | | - | - | 5 | | $V_{DS}=80V, V_{GS}=0, T_J=55^\circ C$ |
| Drain-Source On-Resistance ² | $R_{DS(ON)}$ | - | - | 9 | m Ω | $V_{GS}=10V, I_D=13A$ |
| | | - | - | 12 | | $V_{GS}=4.5V, I_D=11A$ |
| Total Gate Charge(4.5V) | Q_g | - | 21.2 | - | nC | $I_D=13A$ $V_{DS}=50V$ $V_{GS}=10V$ |
| Total Gate Charge | Q_g | - | 45 | - | | |
| Gate-Source Charge | Q_{gs} | - | 9.5 | - | | |
| Gate-Drain Charge | Q_{gd} | - | 4.8 | - | | |
| Turn-On Delay Time | $T_{d(on)}$ | - | 10 | - | nS | $V_{DD}=50V$ $I_D=13A$ $V_{GS}=10V$ $R_G=3\Omega$ |
| Rise Time | T_r | - | 6.5 | - | | |
| Turn-Off Delay Time | $T_{d(off)}$ | - | 45 | - | | |
| Fall Time | T_f | - | 7.5 | - | | |
| Input Capacitance | C_{iss} | - | 3320 | - | pF | $V_{GS}=0V$ $V_{DS}=50V$ $f=1.0MHz$ |
| Output Capacitance | C_{oss} | - | 605 | - | | |
| Reverse Transfer Capacitance | C_{rss} | - | 20 | - | | |
| Source-Drain Diode | | | | | | |
| Continuous Source Current ¹ | I_S | - | - | 13 | A | |
| Pulsed Source Current ² | I_{SM} | - | - | 55 | A | |
| Forward On Voltage ² | V_{SD} | - | - | 1.2 | V | $I_S=1A, V_{GS}=0V, T_J=25^\circ C$ |
| Reverse Recovery Time | T_{rr} | - | 33 | - | nS | $I_S=13A, V_{GS}=0, di/dt=100A/\mu s$ |
| Reverse Recovery Charge | Q_{rr} | - | 150 | - | nC | |

Notes:

- Surface mounted on a 1inch² FR-4 board with 2OZ copper.
- The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
- The power dissipation is limited by 150°C junction temperature.

CHARACTERISTICS CURVE

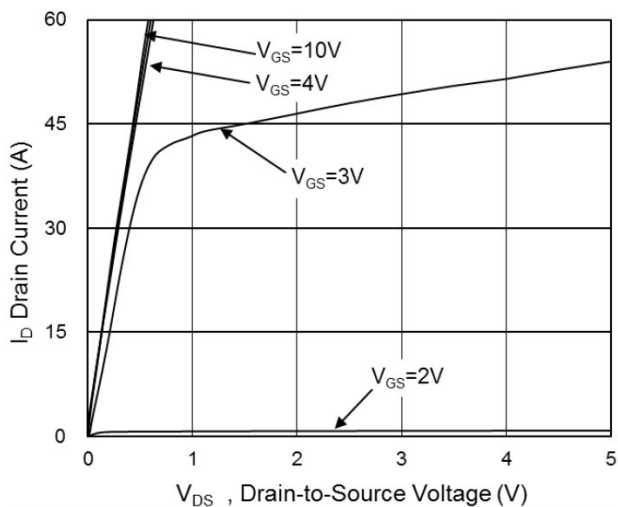


Fig.1 Typical Output Characteristics

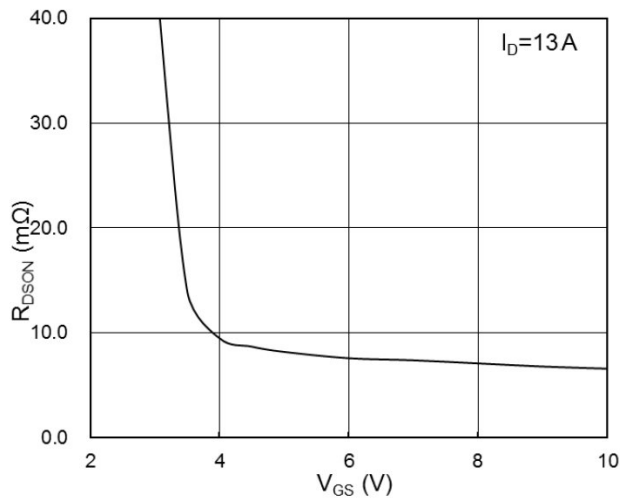


Fig.2 On-Resistance vs. G-S Voltage

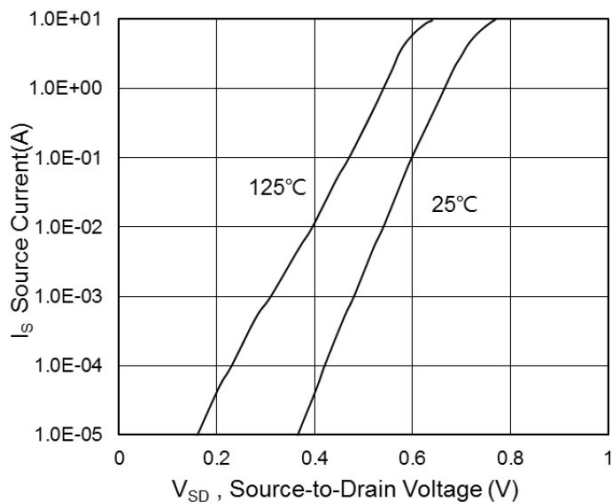


Fig.3 Source-Drain Forward Characteristics

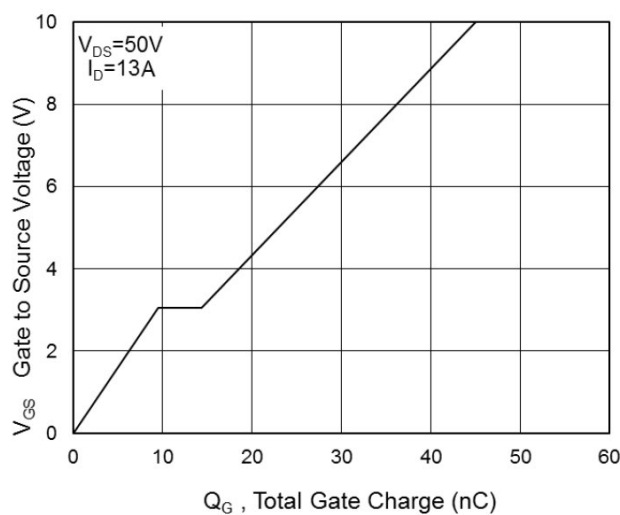


Fig.4 Gate-Charge Characteristics

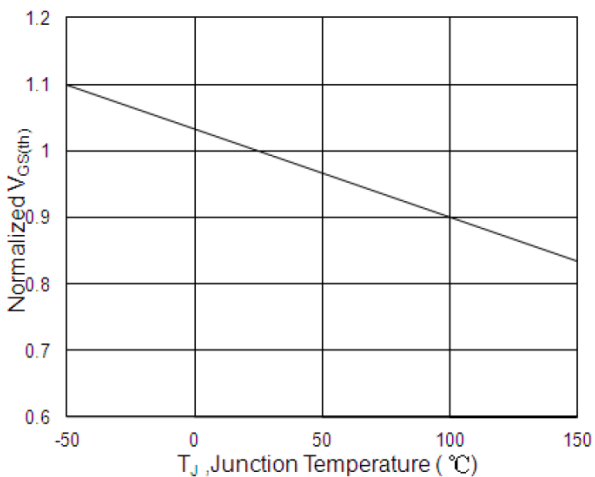


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

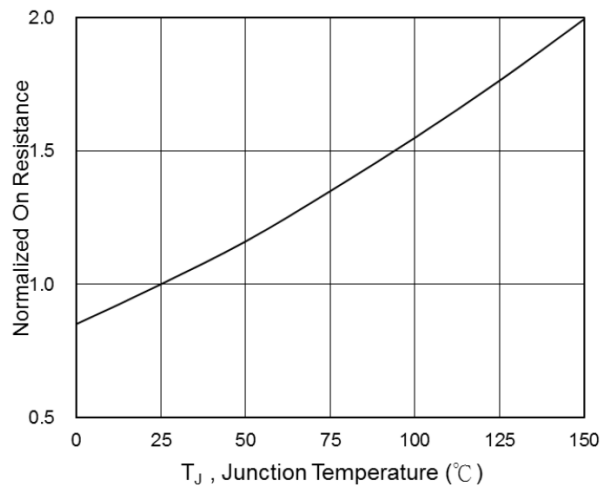


Fig.6 Normalized $R_{DS(ON)}$ vs. T_J

CHARACTERISTICS CURVE

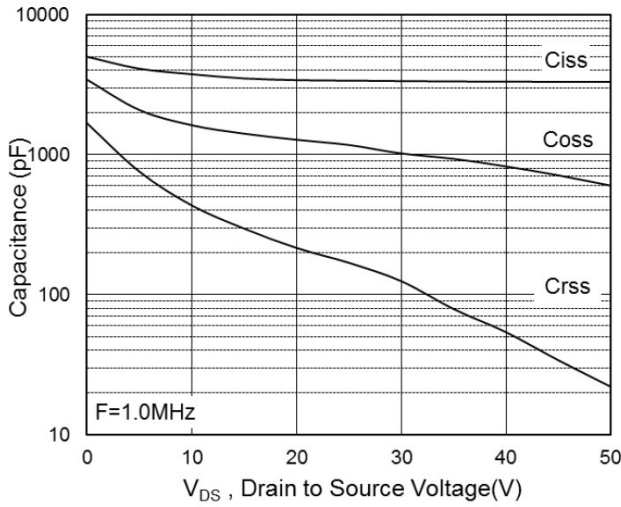


Fig.7 Capacitance

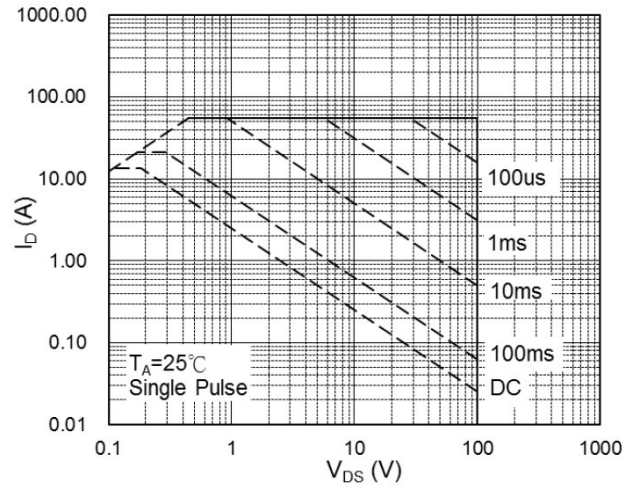


Fig.8 Safe Operating Area

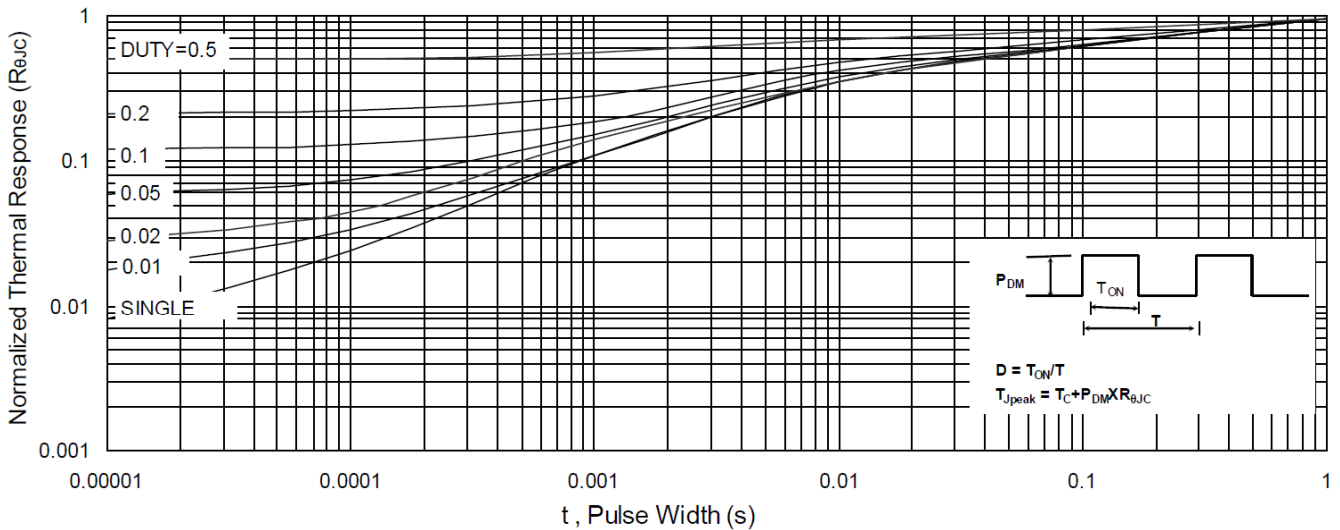


Fig.9 Normalized Maximum Transient Thermal Impedance

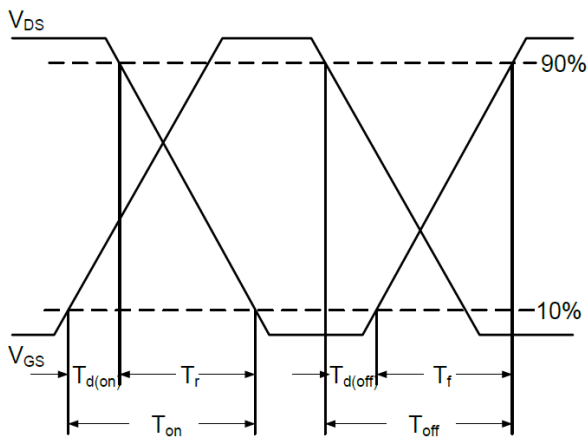


Fig.10 Switching Time Waveform

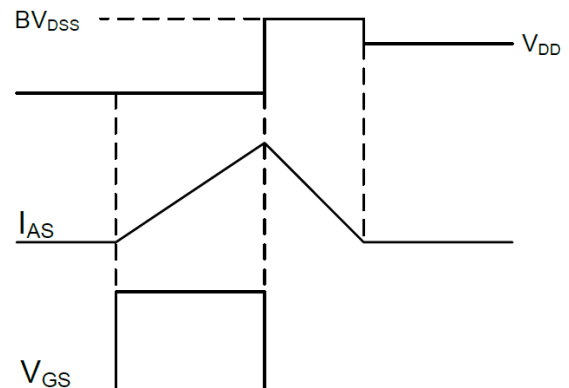


Fig.11 Unclamped Inductive Switching Waveform