

RoHS Compliant Product
A suffix of "-C" specifies halogen free

DESCRIPTION

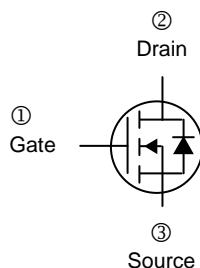
SSA70N90J uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. This device is suitable for the use in a wide variety of applications.

FEATURES

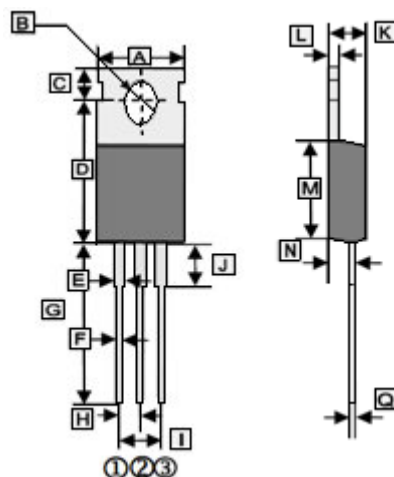
- High density cell design for ultra low $R_{DS(on)}$
- Lead free product is acquired
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special processing technology for high ESD capability

APPLICATION

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



TO-220J-A



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	9.91	10.25	I	4.98	5.18
B	3.4	3.8	J	2.85	3.25
C	2.65	2.95	K	4.4	4.6
D	12.65	12.95	L	1.2	1.4
E	1.17	1.37	M	8.95	9.75
F	0.71	0.91	N	2.25	2.55
G	12.9	13.4	Q	0.33	0.65
H	2.540 TYP.				

ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	70	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	90	A
Pulsed Drain Current ¹	I_{DM}	320	A
Single Pulsed Avalanche Energy ⁴	E_{AS}	580	mJ
Thermal Resistance from Junction to Ambient ²	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature Range	T_J, T_{STG}	150, -55~150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_A=25^\circ\text{C}$ unless otherwise specified)

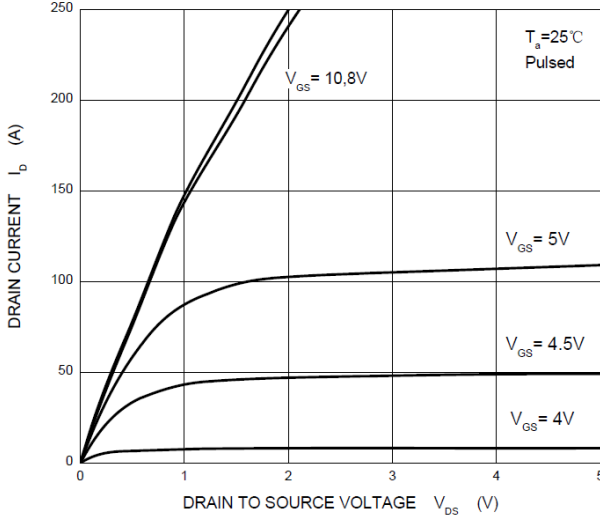
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	70	-	-	V	$V_{GS}=0, I_D=250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	-	-	1	μA	$V_{DS}=70\text{V}, V_{GS}=0$
Gate-Body Leakage Current	I_{GSS}	-	-	± 100	nA	$V_{DS}=0\text{V}, V_{GS}= \pm 20\text{V}$
Gate-Threshold Voltage ³	$V_{GS(th)}$	2	-	4	V	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$
Static Drain-Source On-Resistance ³	$R_{DS(ON)}$	-	-	7.5	m Ω	$V_{GS}=10\text{V}, I_D=40\text{A}$
Forward Transconductance ³	g_{FS}	-	60	-	S	$V_{DS}=5\text{V}, I_D=40\text{A}$
Diode Forward Voltage ³	V_{SD}	-	-	1.2	V	$V_{GS}=0, I_S=20\text{A}$
Dynamic Characteristics						
Input Capacitance	C_{iss}	-	4871	-	pF	$V_{DS}=15\text{V}$ $V_{GS}=0$ $f=1\text{MHz}$
Output Capacitance	C_{oss}	-	630.6	-		
Reverse Transfer Capacitance	C_{rss}	-	410.3	-		
Gate Resistance	R_g	-	0.63	-	Ω	$V_{DS}=V_{GS}=0, f=1\text{MHz}$
Switching Characteristics						
Total Gate Charge	Q_g	-	85.7	-	nC	$V_{DS}=48\text{V}$ $V_{GS}=10\text{V}$ $I_D=84\text{A}$
Gate-Source Charge	Q_{gs}	-	23.2	-		
Gate-Drain Charge	Q_{gd}	-	31.2	-		
Turn-on Delay Time	$T_{d(on)}$	-	36.1	-	nS	$V_{DS}=30\text{V}$ $V_{GS}=10\text{V}$ $R_{GEN}=10\Omega$ $I_D=42\text{A}$
Rise Time	T_r	-	54.3	-		
Turn-off Delay Time	$T_{d(off)}$	-	85.2	-		
Fall Time	T_f	-	37.3	-		
Body Diode Reverse Recovery Time ³	T_{RR}	-	-	88.3	A	$I_F=84\text{A}, di/dt=100\text{A}/\mu\text{s}$
Body Diode Reverse Recovery Charge ³	Q_{RR}	-	-	65.9	A	$I_F=84\text{A}, di/dt=100\text{A}/\mu\text{s}$

Notes:

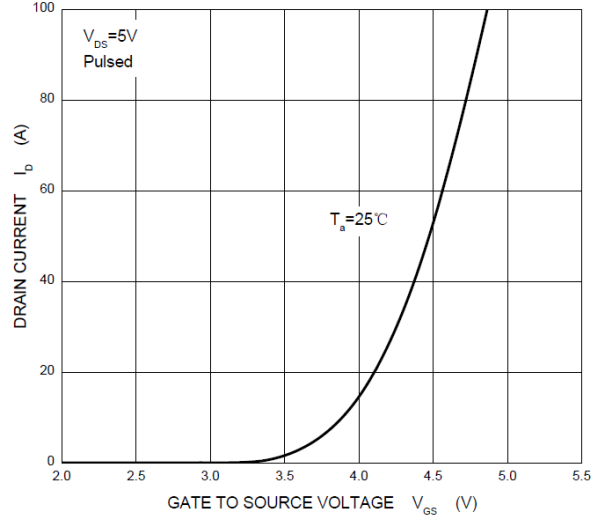
1. Repetitive rating: The pulse width is limited by the junction temperature.
2. The surface of the device is mounted on a FR4 board, $t \leq 10\text{s}$.
3. Pulse test : Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
4. E_{AS} condition: $V_{DD}=37.5\text{V}$, $L=0.5\text{mH}$, $R_G=25\Omega$, starting $T_J=25^\circ\text{C}$.

RATINGS AND CHARACTERISTIC CURVES

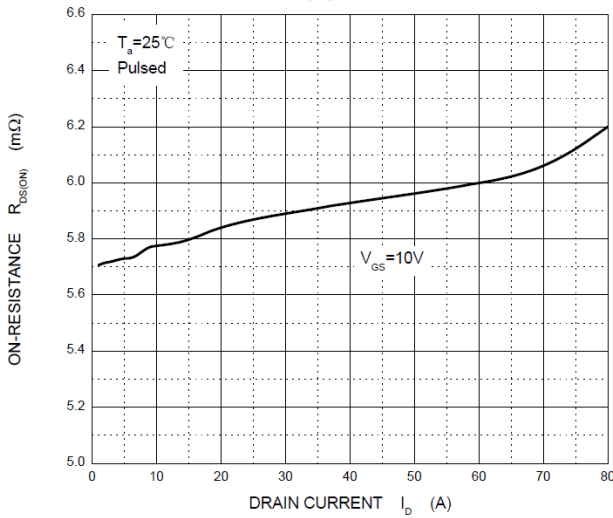
Output Characteristics



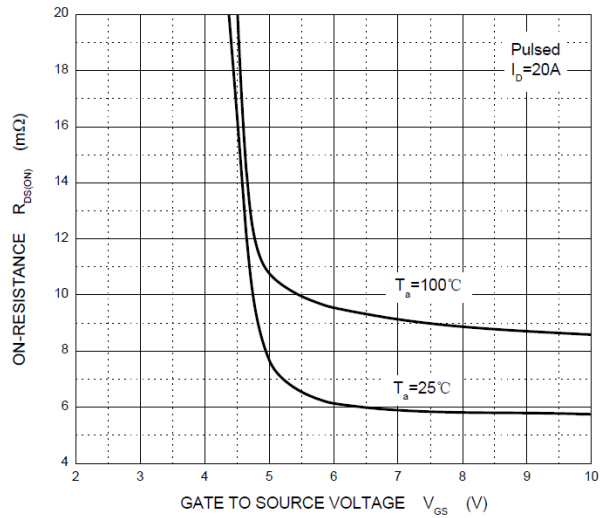
Transfer Characteristics



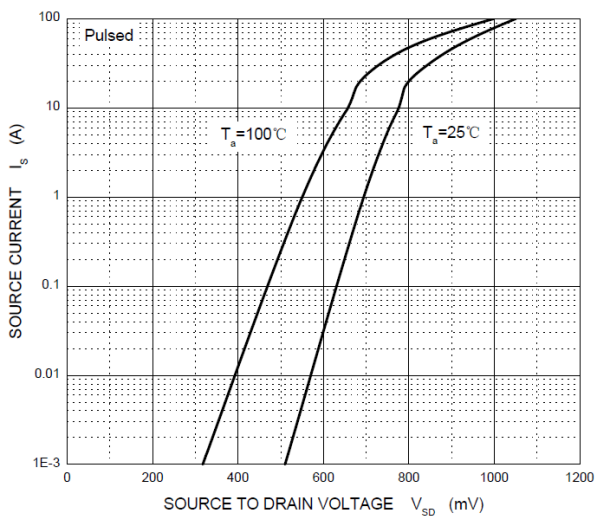
$R_{DS(ON)}$ — I_D



$R_{DS(ON)}$ — V_{GS}



I_S — V_{SD}



Threshold Voltage

