

RoHS Compliant Product  
A suffix of "-C" specifies halogen free

## DESCRIPTION

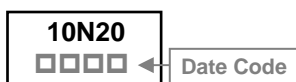
The SSU10N20-C is the highest performance trench N-Ch MOSFETs with extreme high cell density, which provide excellent  $R_{DS(ON)}$  and gate charge for most of the synchronous buck converter applications.

The SSU10N20-C meet the RoHS and Green Product requirement 100% EAS and 100% Rg uaranteed with full function reliability approved.

## FEATURES

- Advanced High Cell Density Trench Technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline

## MARKING



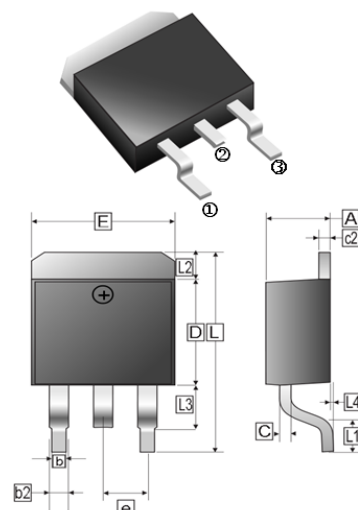
## PACKAGE INFORMATION

Package	MPQ	Leader Size
TO-263	0.8K	13 inch

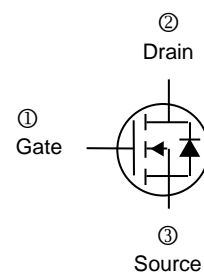
## ORDER INFORMATION

Part Number	Type
SSU10N20-C	Lead (Pb)-free and Halogen-free

## TO-263(D<sup>2</sup>-Pack)



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	4.00	4.87	c2	1.07	1.65
b	0.51	1.01	b2	1.34 REF	
L4	0.00	0.30	D	8.0	9.65
C	0.30	0.74	e	2.54 REF	
L3	1.50 REF		L	14.6	16.1
L1	2.5 REF		L2	1.27 REF	
E	9.60	10.67			



## ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	$V_{DS}$	200	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current, @ $V_{GS}=10V$ <sup>1</sup>	$I_D$	$T_C=25^\circ C$	10
		$T_C=100^\circ C$	5.4
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	20	A
Single Pulse Avalanche Energy <sup>3</sup>	EAS	2	mJ
Avalanche Current	$I_{AS}$	2	A
Total Power Dissipation	$P_D$	$T_C=25^\circ C$	57
		$T_A=25^\circ C$	1.56
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150	$^\circ C$
Thermal Resistance Ratings			
Maximum Thermal Resistance from Junction-Case <sup>1</sup>	$R_{\theta JC}$	2.2	$^\circ C/W$
Maximum Thermal Resistance from Junction-Ambient <sup>1</sup>	$R_{\theta JA}$	80	

**ELECTRICAL CHARACTERISTICS** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

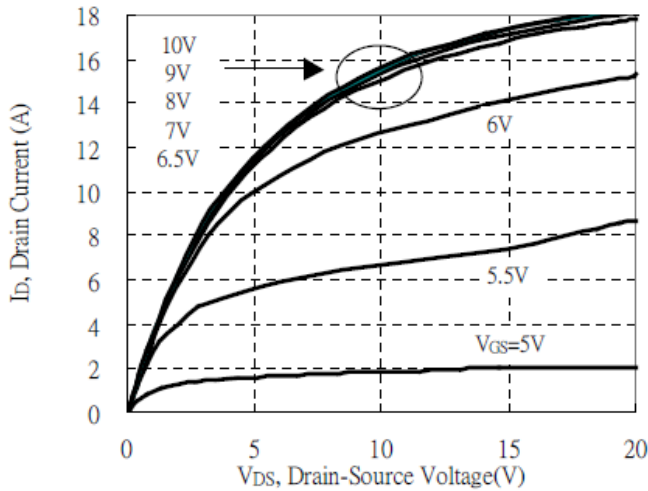
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions	
Drain-Source Breakdown Voltage	$BV_{DSS}$	200	-	-	V	$V_{GS}=0, I_D=250\mu\text{A}$	
Gate-Threshold Voltage	$V_{GS(th)}$	2	-	4.5	V	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	
Forward Transconductance	$g_{fs}$	-	5.3	-	S	$V_{DS}=15\text{V}, I_D=5\text{A}$	
Gate-Source Leakage Current	$I_{GSS}$	-	-	$\pm 100$	nA	$V_{GS}=\pm 20\text{V}$	
Drain-Source Leakage Current	$I_{DSS}$	$T_J=25^\circ\text{C}$	-	-	1	uA	$V_{DS}=200\text{V}, V_{GS}=0$
		$T_J=125^\circ\text{C}$	-	-	25		$V_{DS}=160\text{V}, V_{GS}=0$
Static Drain-Source On-Resistance <sup>4</sup>	$R_{DS(ON)}$	-	-	380	m $\Omega$	$V_{GS}=10\text{V}, I_D=5\text{A}$	
Total Gate Charge	$Q_g$	-	10.6	-	nC	$I_D=3\text{A}$ $V_{DS}=160\text{V}$ $V_{GS}=10\text{V}$	
Gate-Source Charge	$Q_{gs}$	-	2.3	-			
Gate-Drain Charge	$Q_{gd}$	-	3.9	-			
Turn-on Delay Time	$T_{d(on)}$	-	8.8	-	nS	$V_{DS}=100\text{V}$ $I_D=3\text{A}$ $V_{GS}=10\text{V}$ $R_G=25\Omega$	
Rise Time	$T_r$	-	16.8	-			
Turn-off Delay Time	$T_{d(off)}$	-	21.2	-			
Fall Time	$T_f$	-	19.6	-			
Input Capacitance	$C_{iss}$	-	395	-	pF	$V_{GS}=0$ $V_{DS}=25\text{V}$ $f=1\text{MHz}$	
Output Capacitance	$C_{oss}$	-	47	-			
Reverse Transfer Capacitance	$C_{rss}$	-	23	-			
<b>Guaranteed Avalanche Characteristics</b>							
Single Pulse Avalanche Energy <sup>5</sup>	EAS	0.5	-	-	mJ	$V_{DD}=50\text{V}, L=1\text{mH}, I_{AS}=1\text{A}$	
<b>Source-Drain Diode</b>							
Continuous Source Current <sup>1</sup>	$I_S$	-	-	10	A		
Pulsed Source Current <sup>2</sup>	$I_{SM}$	-	-	20			
Diode Forward Voltage <sup>4</sup>	$V_{SD}$	-	-	1.2	V	$I_S=1\text{A}, V_{GS}=0$	
Reverse Recovery Time	$T_{rr}$	-	50	-	nS	$I_F=3\text{A}, T_J=25^\circ\text{C}$ $di/dt=100\text{A}/\mu\text{s}$	
Reverse Recovery Charge	$Q_{rr}$	-	86	-	nC		

Notes:

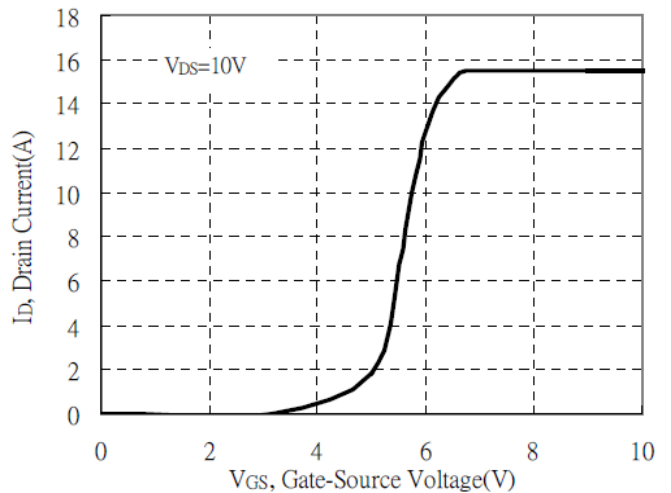
1. The surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
2. Pulse width limited by maximum junction temperature.
3. The EAS data shows Max. rating. The test condition is  $V_{DD}=50\text{V}, V_{GS}=10\text{V}, L=1\text{mH}, I_{AS}=2\text{A}$ .
4. The data tested by pulsed, pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
5. The Min. value is 100% EAS tested guarantee.

**CHARACTERISTIC CURVES**

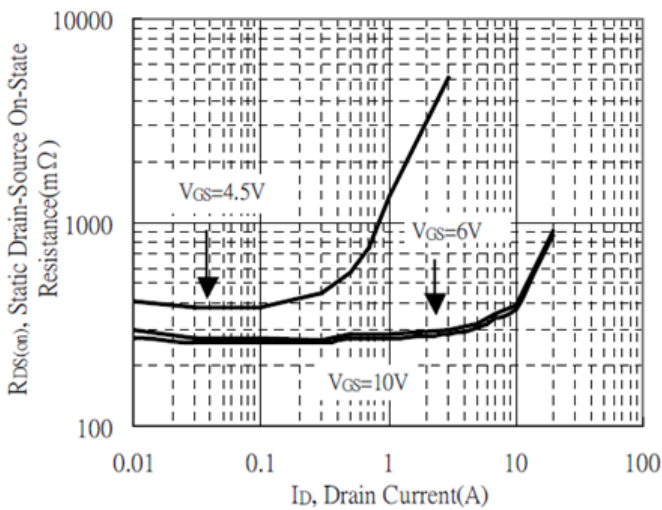
Typical Output Characteristics



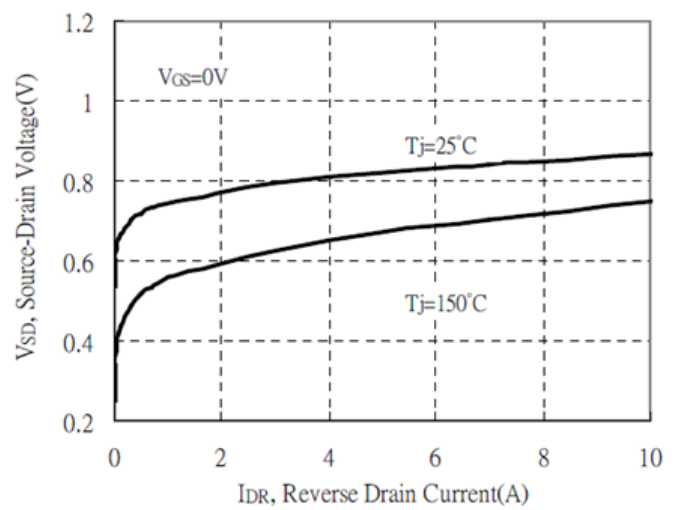
Typical Transfer Characteristics



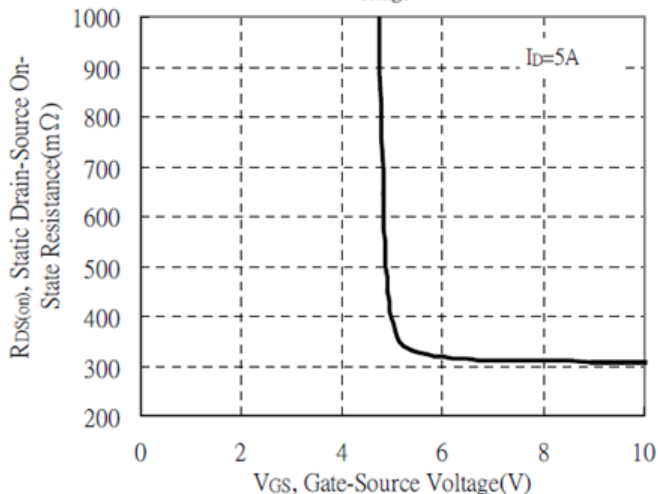
Static Drain-Source On-State resistance vs Drain Current



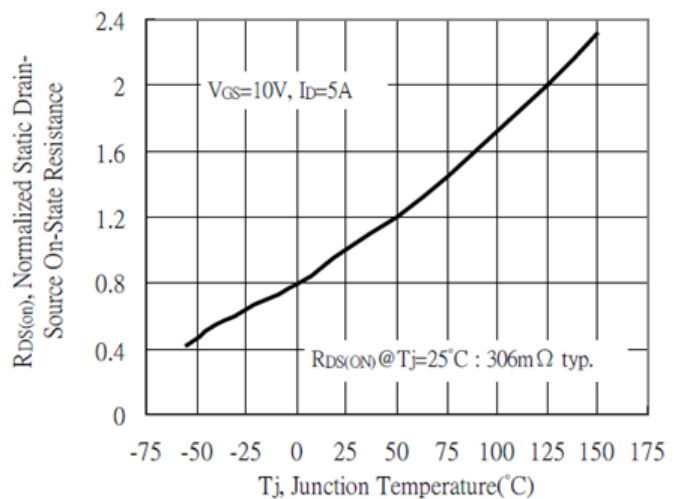
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

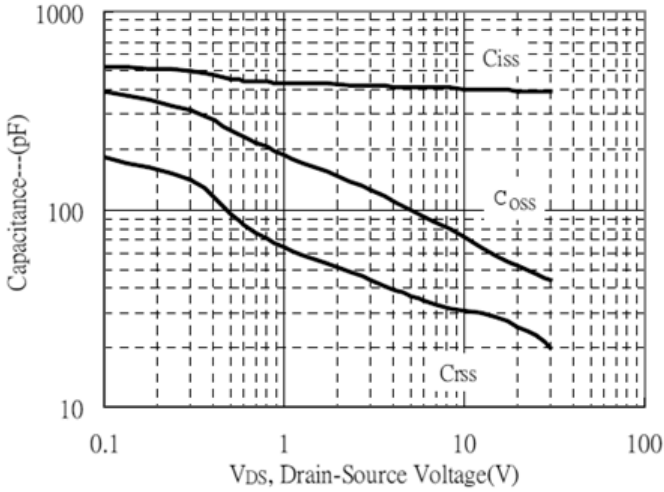


Drain-Source On-State Resistance vs Junction Temperature

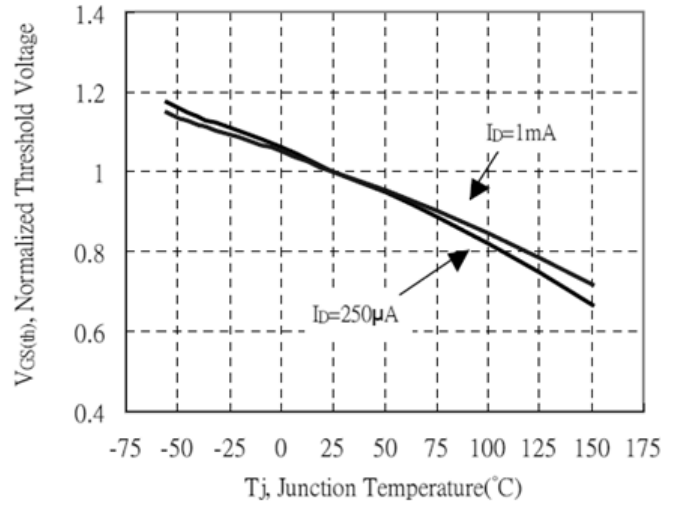


**CHARACTERISTIC CURVES**

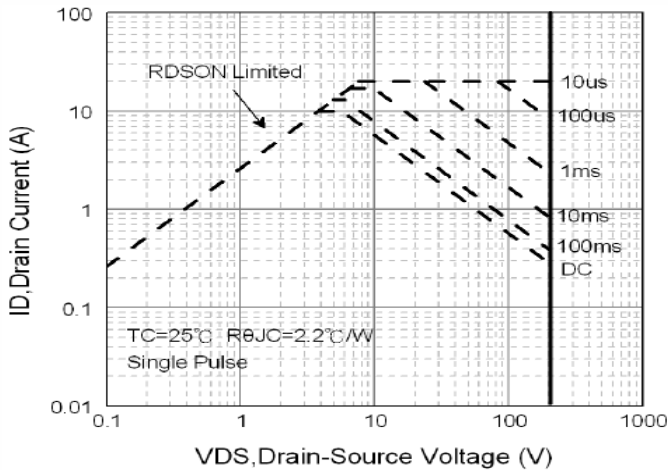
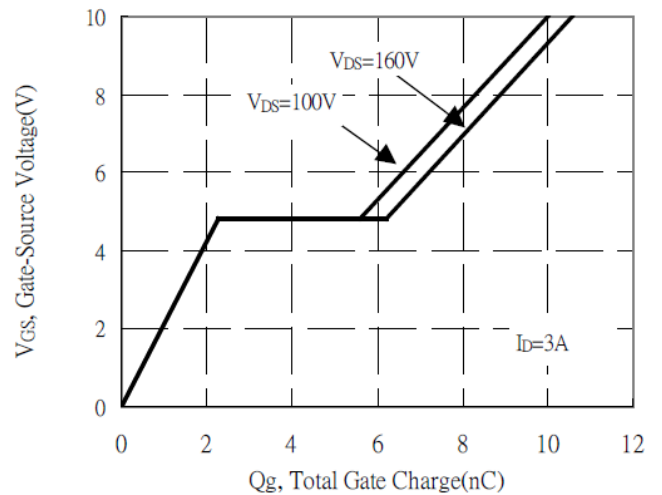
Capacitance vs Drain-to-Source Voltage



Threshold Voltage vs Junction Temperature



Gate Charge Characteristics



Transient Thermal Response Curves

