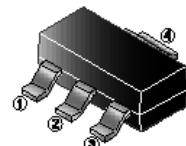


RoHS Compliant Product
A suffix of "-C" specifies halogen and lead-free

DESCRIPTION

The SLMJ317C-C is an adjustable 3-terminal positive voltage regulator, designed to supply more than 1.5A of load current with an output voltage adjustable from 1.25V to 37V. It employs internal current limiting, thermal shut-down, and safe area compensation.

SOT-223



FEATURES

- Output Transistor Safe Operating Area Compensation
- Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting

MARKING

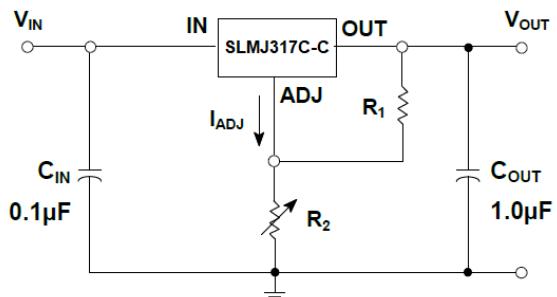


PACKAGE INFORMATION

Package	MPQ	Leader Size
SOT-223	2.5K	13 inch

ORDER INFORMATION

Part Number	Type
SLMJ317C-C	Lead (Pb)-free and Halogen-free



Typical Application Circuits

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Ratings	Unit
Maximum Input-Output Difference Voltage	V _i -V _o	40	V
Programmable Output Voltage Range	V _{OUT}	37	
Output Current Range	I _O	1.5	A
Maximum Power Dissipation	P _D	1	W
Thermal Resistance from Junction-Ambient ¹	R _{θJA}	100	°C/W
Thermal Resistance from Junction-Case ¹	R _{θJC}	27	
Storage Temperature @10s	T _{solder}	260	°C
Junction & Storage Temperature Range	T _J , T _{STG}	-40~125, -65~150	°C

Notes:

1. Thermal metric is measured in still air with T_A=25°C and installed on a 1 in² FR-4 board covered with 2 ounces of copper.

ELECTRICAL CHARACTERISTICS

($V_i-V_o=5V$, $I_o=0.5A$, $C_{IN}=1\mu F$, $C_{OUT}=1\mu F$, unless otherwise specified)

Parameter	Symbol	Test Conditions		Min.	Typ.	Max.	Unit
Line Regulation ¹	LNR	$3V \leq V_i - V_o \leq 40V$	$T_J=25^\circ C$	-	0.01	0.04	%/ V
			$T_J=0 \sim 125^\circ C$	-	0.02	0.07	
Load Regulation ¹	LDR	$V_o < 5V$ $0.01A \leq I_o \leq 1.5A$	$T_J=25^\circ C$	-	5	25	mV
			$T_J=0 \sim 125^\circ C$	-	20	70	
		$V_o \geq 5V$ $0.01A \leq I_o \leq 1.5A$	$T_J=25^\circ C$	-	0.1	0.5	%· V_o
			$T_J=0 \sim 125^\circ C$	-	0.3	1.5	
ADJUST Terminal Current	I_{ADJ}	$T_J=25^\circ C$		-	50	100	μA
Change in ADJUST Terminal Current	ΔI_{ADJ}	$2.5V \leq V_i - V_o \leq 40V$ $0.01A \leq I_o \leq 1.5A$	$T_J=25^\circ C$	-	2	5	μA
Reference Voltage	V_{REF}	$3V \leq V_i - V_o \leq 40V$ $0.01A \leq I_o \leq 1.5A$	$T_J=25^\circ C$	1.2	1.25	1.3	V
Line Regulation of Reference Voltage	LNR V_{REF}	$3V \leq V_i - V_o \leq 40V$	$T_J=25^\circ C$	-	0.02	0.07	%/ V
Load Regulation of Reference Voltage	LDR V_{REF}	$V_o < 5V$ $0.01A \leq I_o \leq 1.5A$	$T_J=25^\circ C$	-	20	70	mV
		$V_o \geq 5V$ $0.01A \leq I_o \leq 1.5A$	$T_J=25^\circ C$	-	0.3	1.5	%· V_o
Output Voltage Temperature Stability	$\Delta V_{OUT} / V_{OUT}$	$T_J=0 \sim 125^\circ C$		-	1	-	%
Minimum Load Current to Maintain Regulation	$I_o(MIN)$	$V_i - V_o = 40V$	$T_J=0 \sim 125^\circ C$	-	3.5	10	mA
Maximum Output Current	$I_o(MAX)$	$V_i - V_o \leq 15V$	$T_J=0 \sim 125^\circ C$	1.5	2.2	-	A
		$V_i - V_o = 40V$	$T_J=25^\circ C$	0.15	0.4	-	
Output Noise Voltage @% of V_{OUT}	eN	$10Hz \leq f \leq 10kHz$	$T_J=25^\circ C$	-	0.003	-	%
Ripple Rejection	RR	$f=100Hz$	$C_{ADJ}=0\mu F^2$	-	60	-	dB
			$C_{ADJ}=10\mu F^2$	-	65	-	
		$f=1kHz$	$C_{ADJ}=0\mu F^2$	-	64	-	
			$C_{ADJ}=10\mu F^2$	-	75	-	

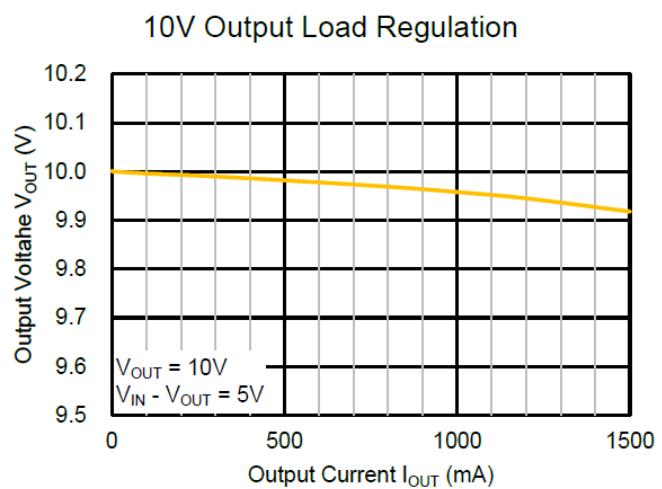
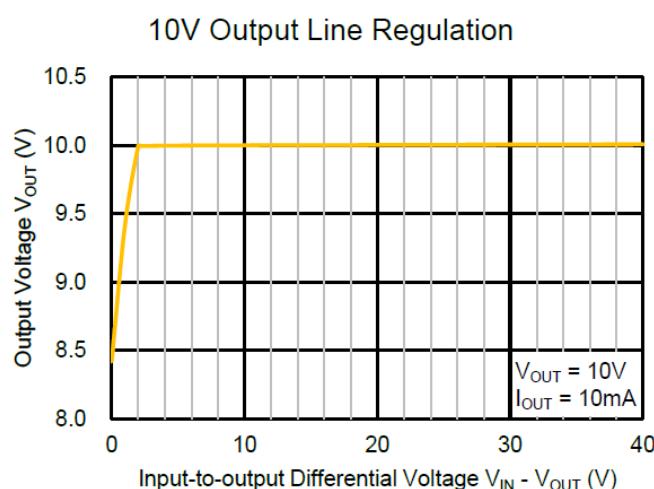
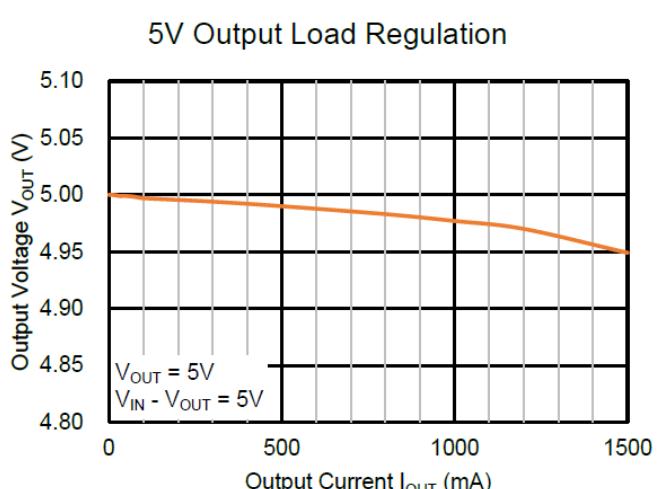
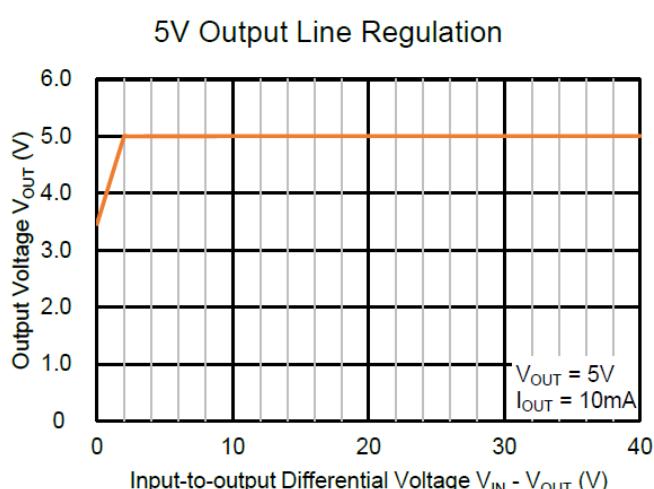
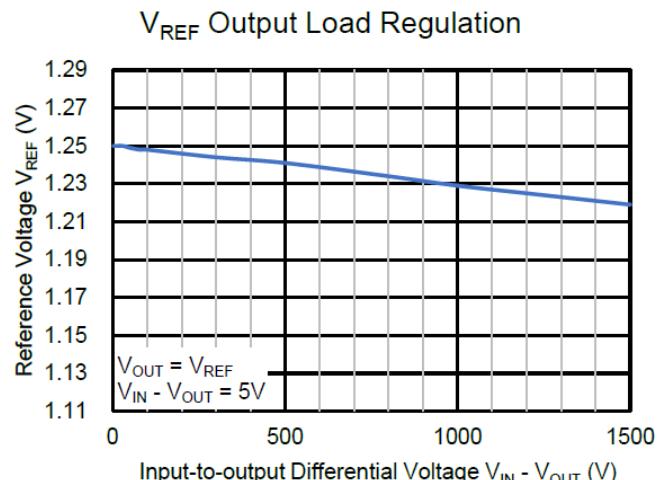
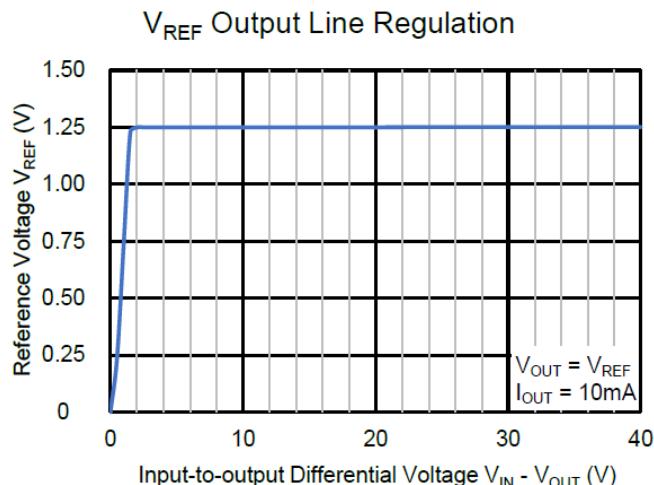
Notes:

- The line regulation is calculated by the following formula:

$$LNR = \Delta V_{OUT} / V_{OUT} \times \Delta V_{IN}$$
 where, ΔV_{OUT} is the variation of the output voltage, ΔV_{IN} is the variation of the input voltage.
- C_{ADJ} is connected between the ADJ terminal and GND.

TYPICAL CHARACTERISTICS

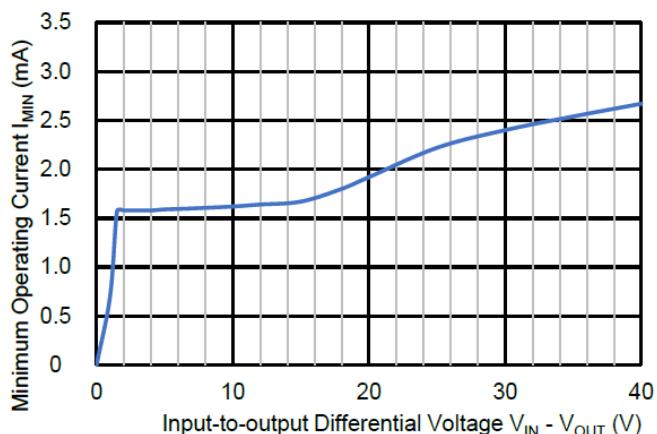
($C_{IN} = 0.1\mu F$, $C_{OUT} = 1\mu F$, unless otherwise specified)



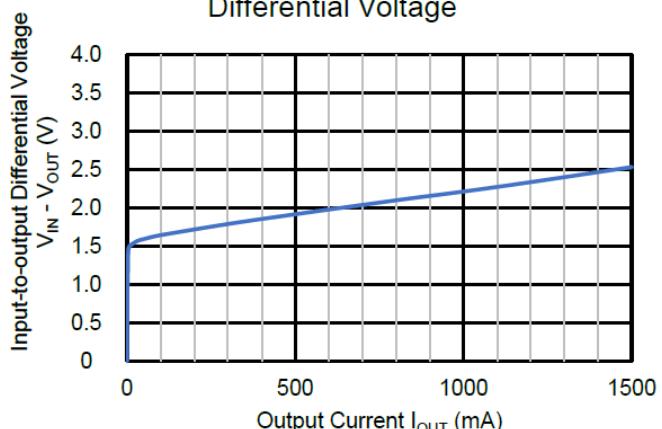
TYPICAL CHARACTERISTICS

($C_{IN} = 0.1\mu F$, $C_{OUT} = 1\mu F$, unless otherwise specified)

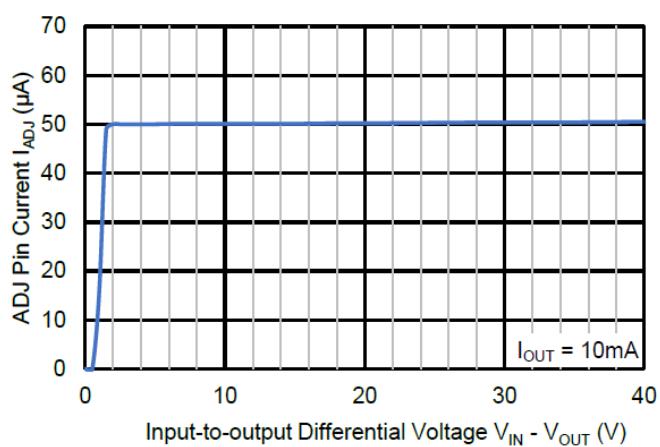
Minimum Operating Current



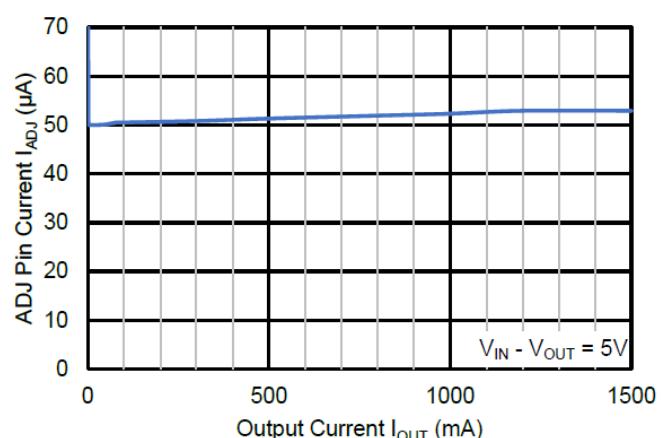
Minimum Operating Input-to-output Differential Voltage



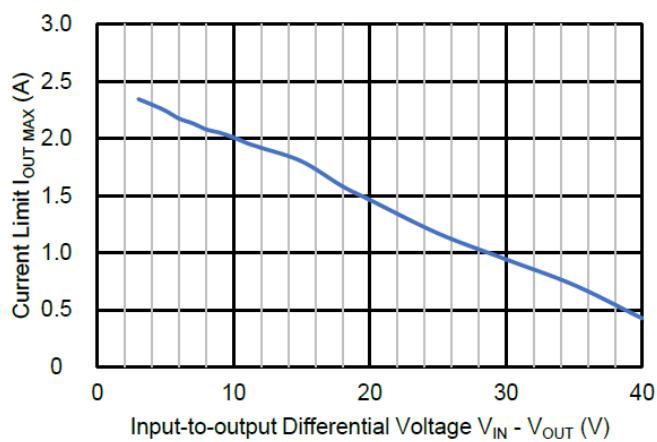
ADJ Pin Current



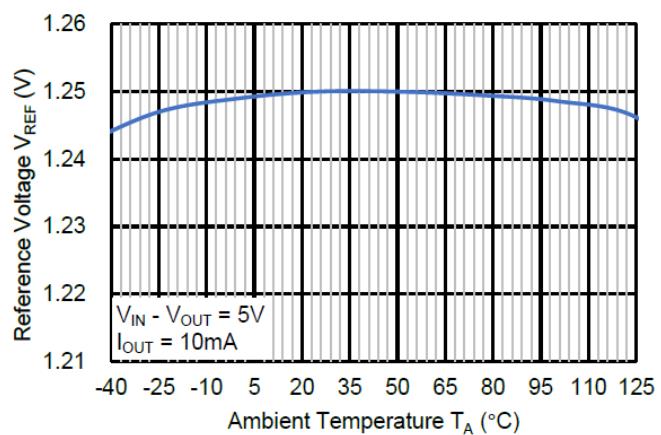
ADJ Pin Current



Current Limit



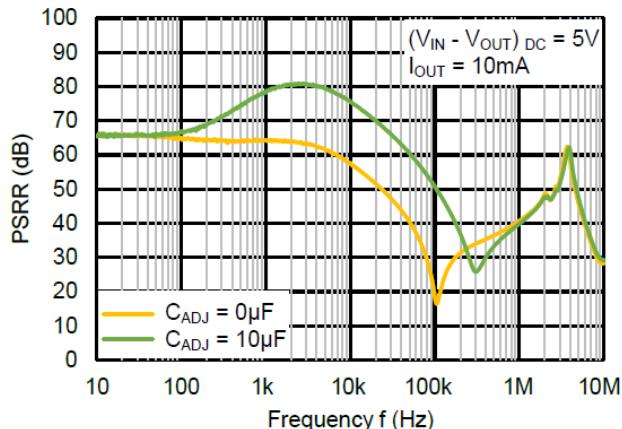
Temperature Characteristics



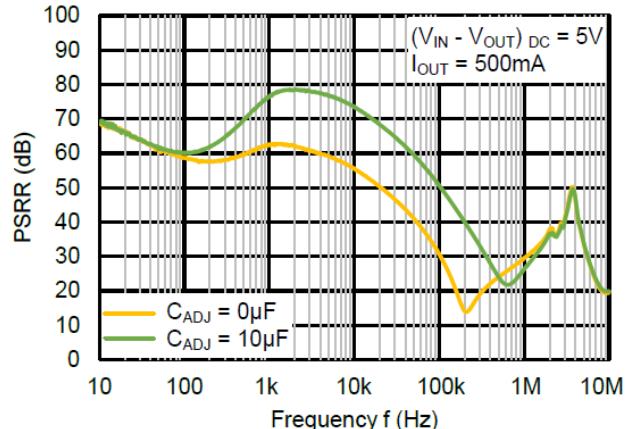
TYPICAL CHARACTERISTICS

($C_{IN} = 0.1\mu F$, $C_{OUT} = 1\mu F$, unless otherwise specified)

Power Supply Rection Ratio

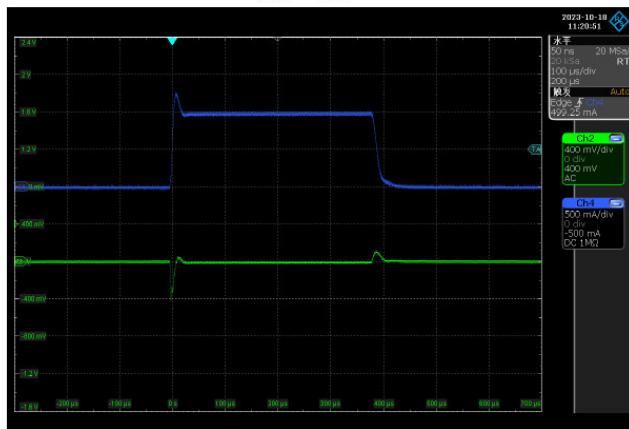


Power Supply Rection Ratio

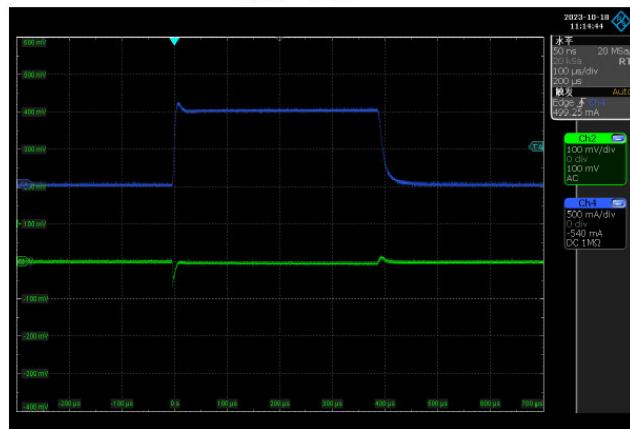


Load Transient ($V_{OUT} = 10\text{V}$, $V_{IN} - V_{OUT} = 5\text{V}$, $I_{OUT} = 10 \sim 1000\text{mA}$, CH2: V_{OUT} , CH4: I_{OUT})

$C_{ADJ} = 0\mu\text{F}$



$C_{ADJ} = 10\mu\text{F}$

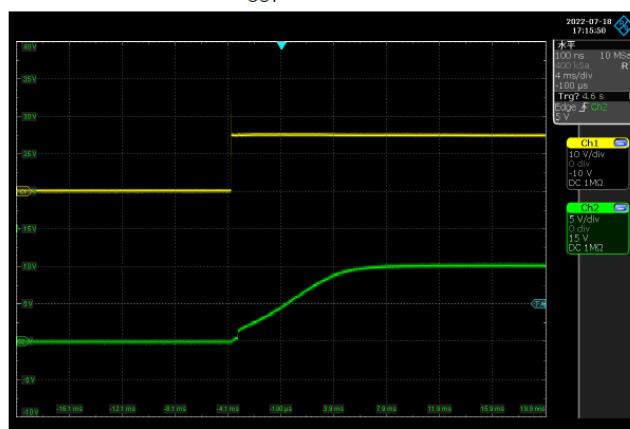


Power up Response ($V_{OUT} = 10\text{V}$, $V_{IN} = 0 \sim (V_{OUT} + 5\text{V})$, $C_{ADJ} = 10\mu\text{F}$, CH1: V_{IN} , CH2: V_{OUT})

$I_{OUT} = 10\text{mA}$

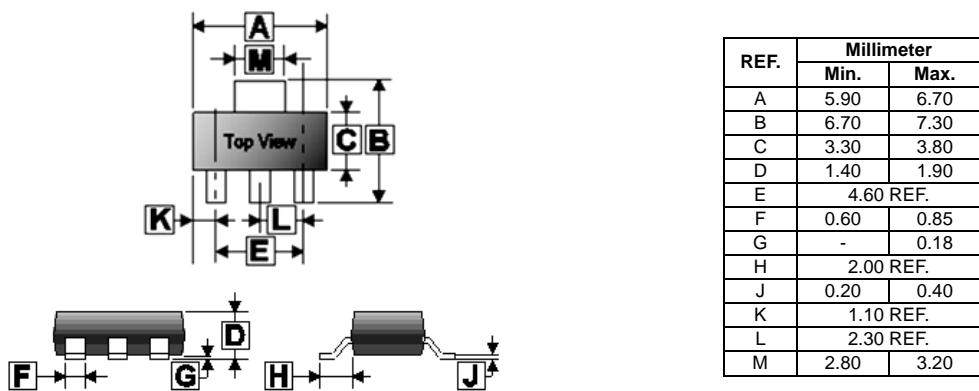


$I_{OUT} = 1000\text{mA}$



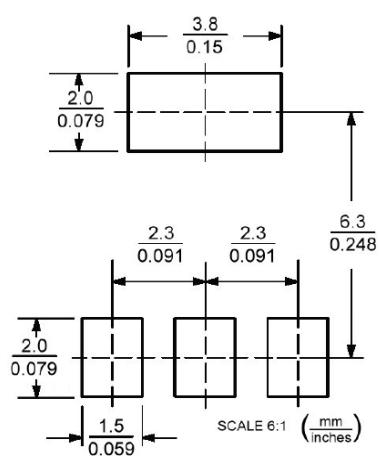
PACKAGE OUTLINE DIMENSIONS

SOT-223



MOUNTING PAD LAYOUT

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*Dimensions in millimeters