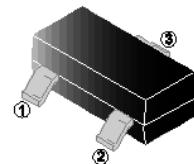


RoHS Compliant Product
A suffix of "C" specifies halogen & lead-free

FEATURES

- Trench Power MV MOSFET Technology
- High Power and current handing capability
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating

SOT-23



APPLICATION

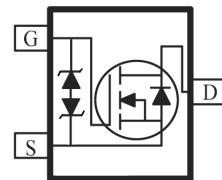
- PWM Application
- Load Switch

MARKING

2304B.

PACKAGE INFORMATION

Package	MPQ	Leader Size
SOT-23	3K	7 inch



ORDER INFORMATION

Part Number	Type
SMS2304B-C	Lead (Pb)-free and Halogen-free

ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current	I _D	4	A
		2.5	
Pulsed Drain Current ¹	I _{DM}	20	A
Total Power Dissipation ²	P _D	1	W
		0.4	
Thermal Resistance from Junction-Ambient ³	R _{θJA}	125	°C/W
Operating Junction & Storage Temperature Range	T _J , T _{STG}	-55~150	°C

Notes:

1. Repetitive rating; pulse width limited by max. Junction temperature.
2. P_D is based on max. Junction temperature, using junction-case thermal resistance.
3. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz. Copper, in the still air environment with T_A=25°C. The maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.

ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	30	-	-	V	$V_{GS}=0$, $I_D=250\mu\text{A}$
Gate Threshold Voltage	$V_{GS(\text{th})}$	1	1.6	2.2	V	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$
Gate-Body Leakage Current	I_{GSS}	-	-	± 100	μA	$V_{GS} = \pm 20\text{V}$, $V_{DS}=0$
Zero Gate Voltage Drain Current	I_{DSS}	-	-	1	μA	$V_{DS}=30\text{V}$, $V_{GS}=0$
$T_J=150^\circ\text{C}$		-	-	100		
Drain-Source On-Resistance	$R_{DS(\text{ON})}$	-	28	36	$\text{m}\Omega$	$V_{GS}=10\text{V}$, $I_D=4\text{A}$
		-	46	60		$V_{GS}=4.5\text{V}$, $I_D=3\text{A}$
Gate resistance	R_G	-	5	-	Ω	$f=1\text{MHz}$, Open drain
Total Gate Charge	Q_g	-	6.5	-	nC	$V_{GS}=10\text{V}$, $V_{DS}=15\text{V}$, $I_D=4\text{A}$
Gate-Source Charge	Q_{gs}	-	1	-		
Gate-Drain Charge	Q_{gd}	-	2	-		
Turn-on Delay Time	$t_{(\text{on})}$	-	3	-	nS	$V_{GS}=10\text{V}$, $V_{DD}=15\text{V}$, $I_D=4\text{A}$ $R_G=2.2\Omega$
Rise Time	t_r	-	15	-		
Turn-off Delay Time	$t_{(\text{off})}$	-	12	-		
Fall Time	t_f	-	3	-		
Input Capacitance	C_{iss}	-	280	-	pF	$V_{DS}=15\text{V}$ $V_{GS}=0$ $f=1\text{MHz}$
Output Capacitance	C_{oss}	-	45	-		
Reverse Transfer Capacitance	C_{rss}	-	35	-		
Source-Drain Diode						
Diode Forward Voltage	V_{SD}	-	0.9	1.2	V	$V_{GS}=0$, $I_S=4\text{A}$
Maximum Body-Diode Continuous Current	I_S	-	-	4	A	
Reverse Recovery Time	T_{rr}	-	6.5	-	nS	$I_F=4\text{A}$ $dI/dt=300\text{A}/\mu\text{s}$
Recovered Charge	Q_r	-	5	-	nC	

TYPICAL CHARACTERISTICS

Figure 1. Output Characteristics

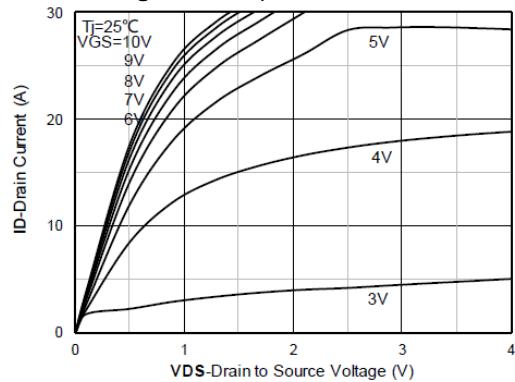


Figure 3. Capacitance Characteristics

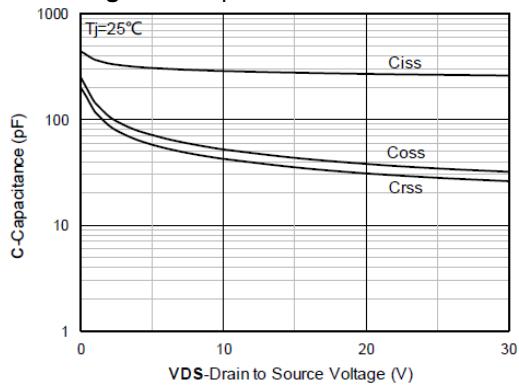


Figure 5. On-Resistance vs Gate to Source Voltage

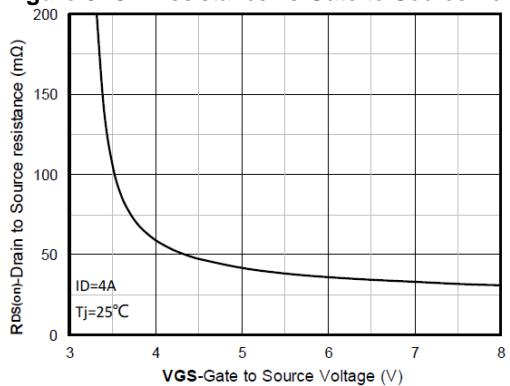


Figure 7. $R_{DS(on)}$ VS Drain Current

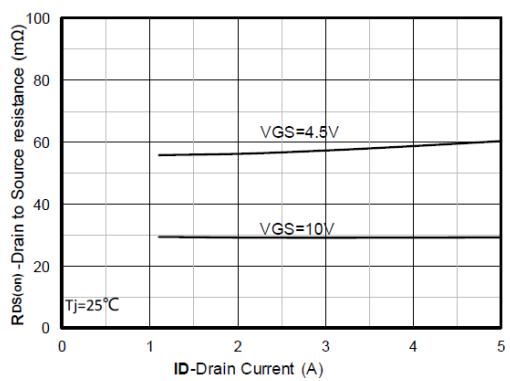


Figure 2. Transfer Characteristics

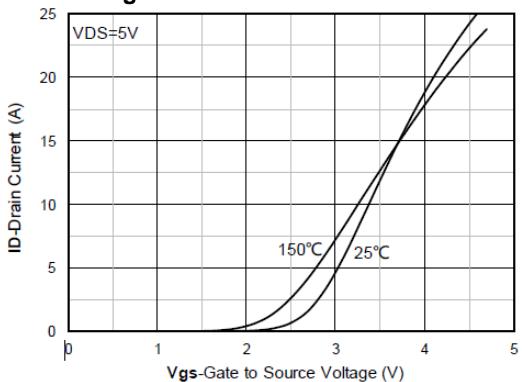


Figure 4. Gate Charge

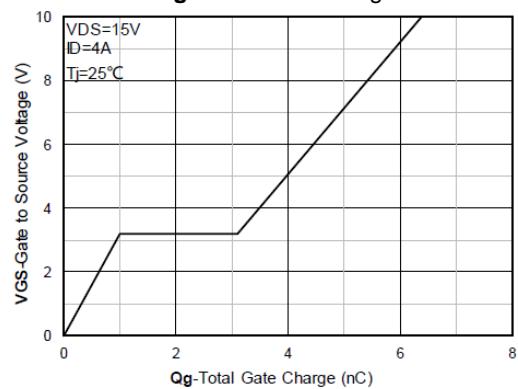


Figure 6. Normalized On-Resistance

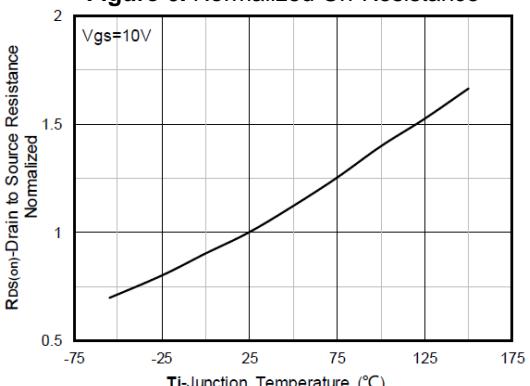
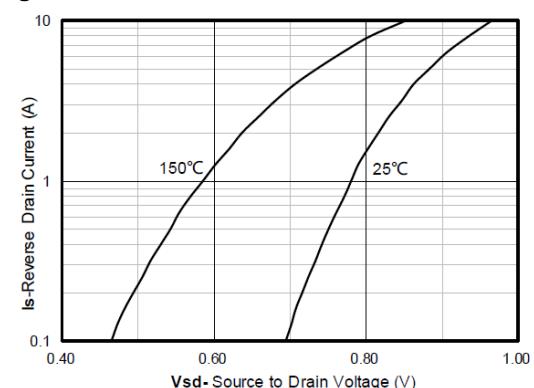


Figure 8. Forward Characteristics of Reverse Diode



TYPICAL CHARACTERISTICS

Figure 9. Normalized Breakdown Voltage

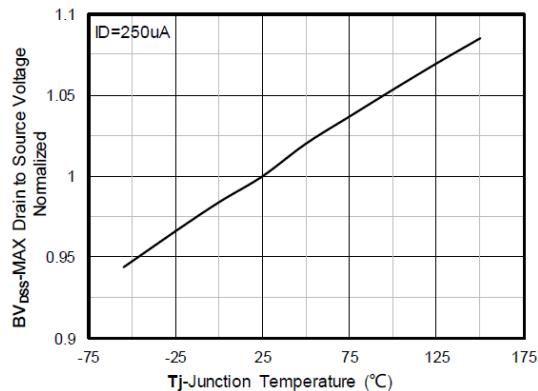


Figure 10. Normalized Threshold Voltage

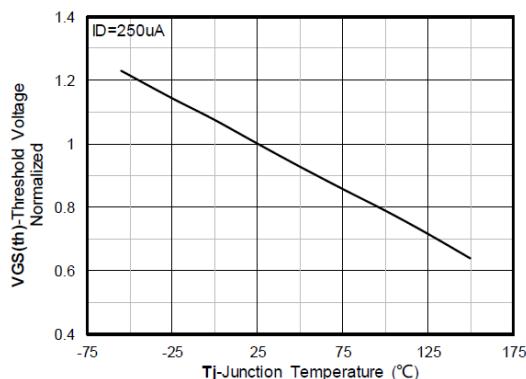


Figure 11. Current Dissipation

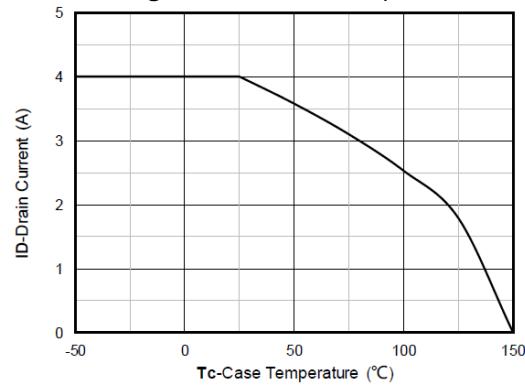


Figure 12. Power Dissipation

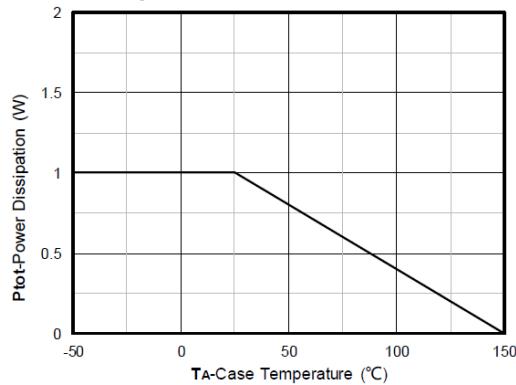


Figure 13. Maximum Transient Thermal Impedance

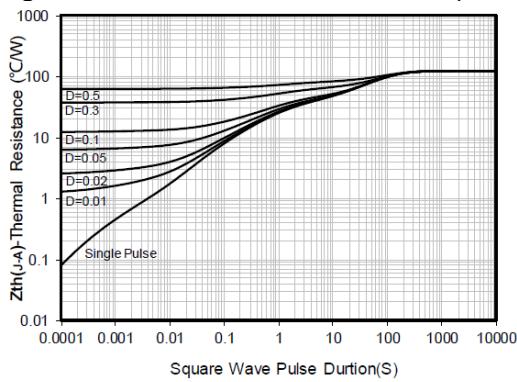
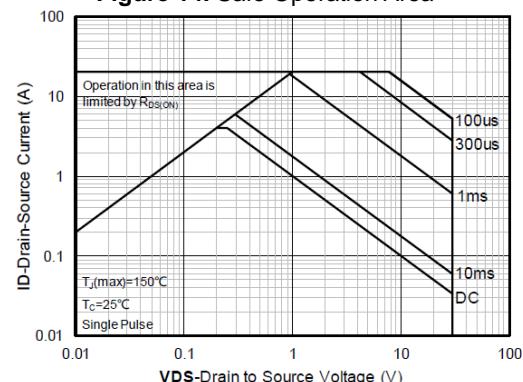
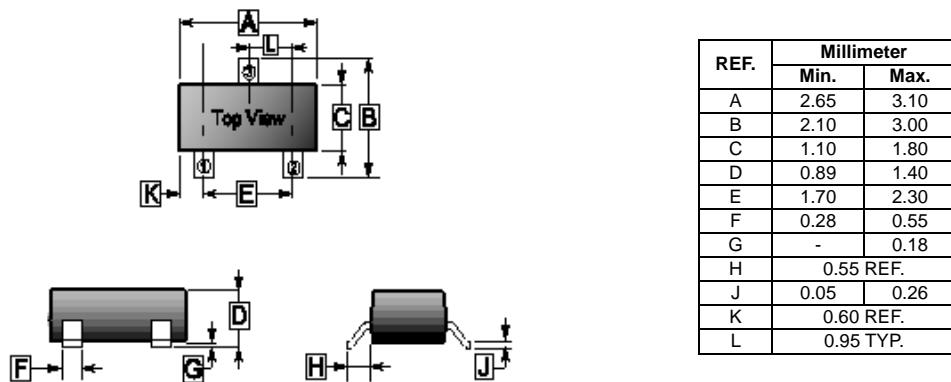


Figure 14. Safe Operation Area



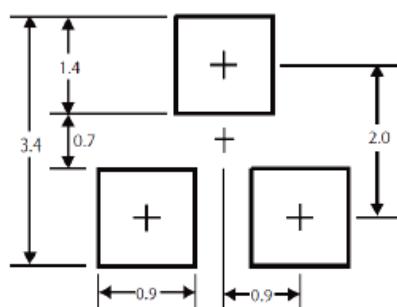
PACKAGE OUTLINE DIMENSIONS

SOT-23



MOUNTING PAD LAYOUT

SOT-23



*Dimensions in millimeters