

RoHS Compliant Product
A suffix of "-C" specifies halogen & lead-free

DESCRIPTION

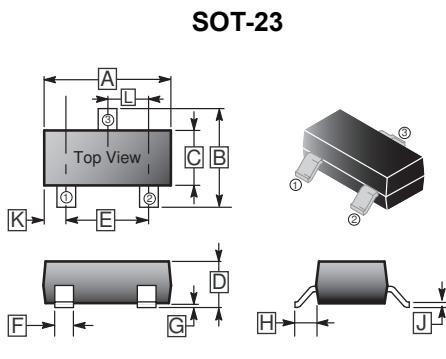
SMS4021-C uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. This device is suitable for the use as a load switch or in PWM applications. The source leads are separated to allow a Kelvin connection to the source, which may be used to bypass the source inductance.

FEATURES

- Lower Gate Charge
- Simple Drive Requirement
- Fast Switching Characteristic

MARKING

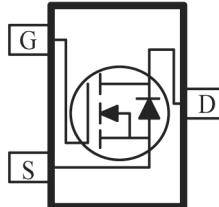
4021



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	2.80	3.04	G	0.09	0.18
B	2.10	2.55	H	0.45	0.60
C	1.20	1.40	J	0.08	0.177
D	0.89	1.15	K	0.6	REF.
E	1.78	2.04	L	0.89	1.02
F	0.30	0.50			

PACKAGE INFORMATION

Package	MPQ	Leader Size
SOT-23	3K	7 inch



ORDER INFORMATION

Part Number	Type
SMS4021-C	Lead (Pb)-free and Halogen-free

ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ²	I_D	4.5	A
Pulsed Drain Current ¹	I_{DM}	10	A
Power Dissipation ²	P_D	1.4	W
Thermal Resistance from Junction to Ambient ²	$R_{\theta JA}$	89	$^\circ\text{C} / \text{W}$
Operating Junction and Storage Temperature	T_J, T_{STG}	150, -55~150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$ unless otherwise specified)

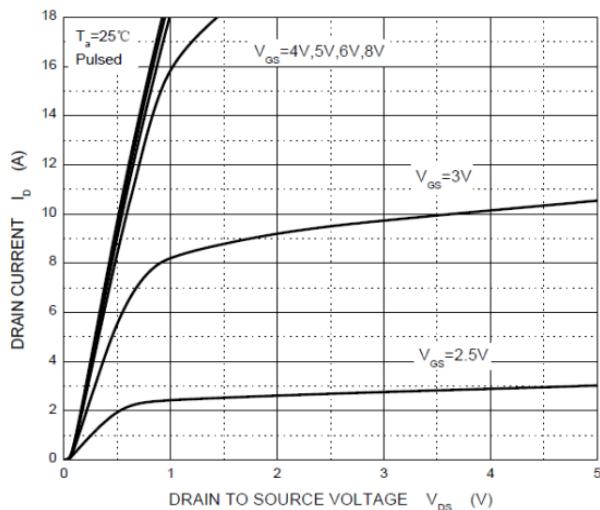
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Drain-Source Breakdown Voltage	BV_{DSS}	30	-	-	V	$\text{V}_{\text{GS}}=0, \text{I}_D=250\mu\text{A}$
Gate-Threshold Voltage	$\text{V}_{\text{GS(th)}}$	1	-	2.5	V	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$
Drain-Source Leakage Current	I_{DSS}	-	-	1	μA	$\text{V}_{\text{DS}}=30\text{V}, \text{V}_{\text{GS}}=0$
Gate-Source Leakage Current	I_{GSS}	-	-	± 100	nA	$\text{V}_{\text{GS}}= \pm 20\text{V}, \text{V}_{\text{DS}}=0$
Static Drain-Source On-Resistance ³	$\text{R}_{\text{DS(ON)}}$	-	20	29	$\text{m}\Omega$	$\text{V}_{\text{DS}}=10\text{V}, \text{I}_D=4.5\text{A}$
		-	37	48		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=4.5\text{A}$
Total Gate Charge	Q_g	-	4.7	-	nC	$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=4.5\text{A}, \text{V}_{\text{DS}}=15\text{V}$
Total Gate Charge	Q_g	-	7.96	-		$\text{I}_D=4.5\text{A}$
Gate-Source Charge	Q_{gs}	-	1.69	-		$\text{V}_{\text{DS}}=15\text{V}$
Gate-Drain Change	Q_{gd}	-	1.52	-		$\text{V}_{\text{GS}}=10\text{V}$
Turn-on Delay Time	$\text{T}_{\text{d(on)}}$	-	4.8	-	nS	$\text{V}_{\text{DS}}=15\text{V}$ $\text{V}_{\text{GS}}=10\text{V}$ $\text{I}_D=1\text{A}$ $\text{R}_{\text{GEN}}=6\Omega$
Rise Time	T_r	-	22	-		
Turn-off Delay Time	$\text{T}_{\text{d(off)}}$	-	10.8	-		
Fall Time	T_f	-	22.6	-		
Input Capacitance	C_{iss}	-	388	-	pF	$\text{V}_{\text{GS}}=0$ $\text{V}_{\text{DS}}=15\text{V}$ $f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	-	67	-		
Reverse Transfer Capacitance	C_{rss}	-	56	-		
Source-Drain Diode						
Diode Forward Voltage ³	V_{SD}	-	-	1.2	V	$\text{I}_s=0.8\text{A}, \text{V}_{\text{GS}}=0$

Notes:

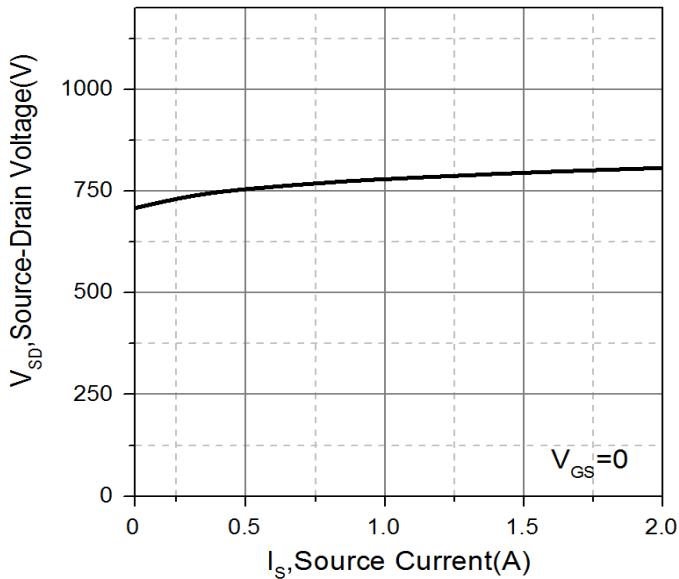
1. Pulse width limited by maximum junction temperature, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
2. The data tested by surface mounted on a 1 inch² FR4 board with 2OZ copper.
3. Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.

CHARACTERISTIC CURVES

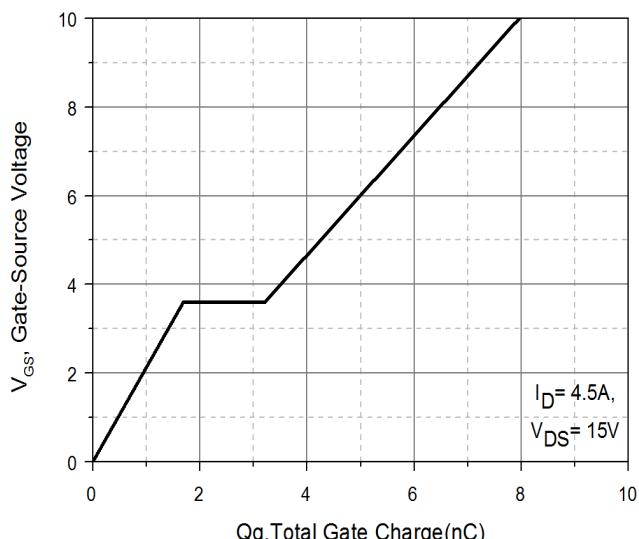
Output Characteristics



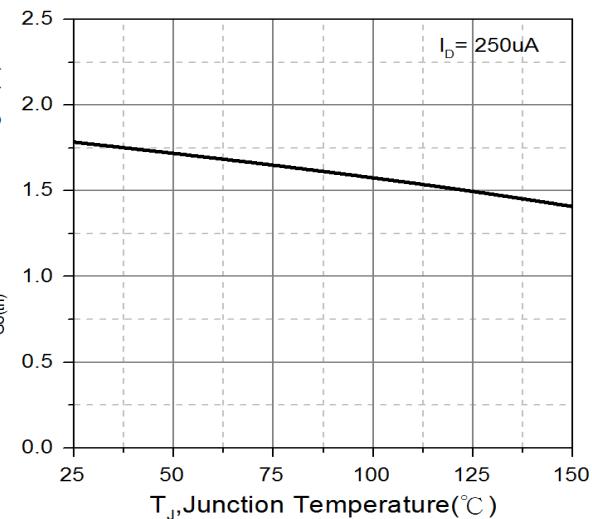
Source-Drain Voltage vs Source Current



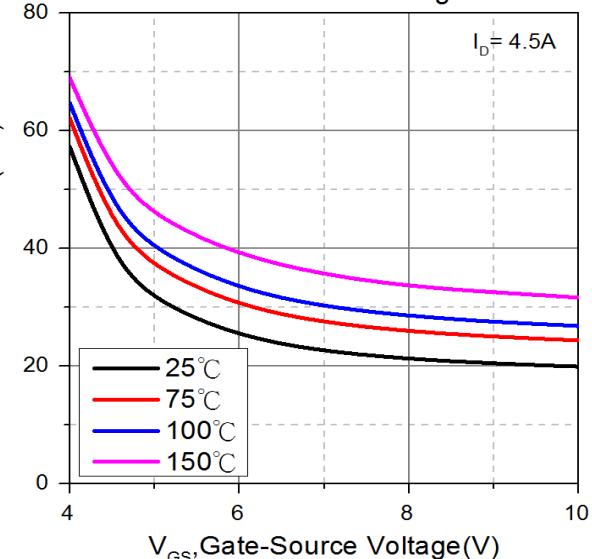
Gate Charge Characteristics



Threshold Voltage vs Junction Temperature



Static Drain-Source On-State Resistance vs Gate-Source Voltage



Capacitance vs Drain-to-Source Voltage

