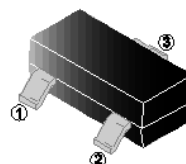


RoHS Compliant Product
A suffix of "-C" specifies halogen & lead-free

FEATURES

- Exceptional On-Resistance and Maximum DC Current Capability
- Super High Density Cell Design for Extremely Low $R_{DS(ON)}$

SOT-23



APPLICATION

- Portable equipment
- Load Switch
- Battery Powered System
- Power Management in Note

MARKING

HYWXZ

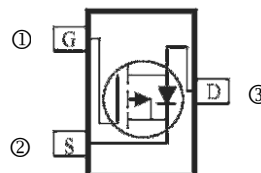
Y : Year
W : Week
XZ : Serial number

PACKAGE INFORMATION

Package	MPQ	Leader Size
SOT-23	3K	7 inch

ORDER INFORMATION

Part Number	Type
SMS4A1P03-C	Lead (Pb)-free and Halogen-free



ABSOLUTE MAXIMUM RATINGS ($T_C=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	-4.1	A
Pulsed Drain Current	I_{DM}	-16.4	A
Power Dissipation	P_D	1.56	W
Operating Junction & Storage Temperature Range	T_J, T_{STG}	-55~150	$^{\circ}\text{C}$
Thermal Resistance Ratings			
Thermal Resistance Junction-Ambient	$R_{\theta JA}$	80	$^{\circ}\text{C/W}$

ELECTRICAL CHARACTERISTICS ($T_J=25^\circ C$ unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	V _{(BR)DSS}	-30	-	-	V	V _{GS} =0V, I _D = -250μA
Gate-Threshold Voltage	V _{GS(th)}	-1	-	-2.5	V	V _{DS} =V _{GS} , I _D = -250μA
Gate-Source Leakage Current	I _{GSS}	-	-	±100	nA	V _{GS} =±20V, V _{DS} =0V
Drain-Source Leakage Current	I _{DSS}	-	-	-1	μA	V _{DS} = -30V, V _{GS} =0V
Static Drain-Source On-Resistance	R _{DS(ON)}	-	-	55	mΩ	V _{GS} = -10V, I _D = -3A
		-	-	85		V _{GS} = -4.5V, I _D = -2A
Total Gate Charge ²	Q _g	-	5.1	-	nC	V _{GS} = -4.5V V _{DS} = -15V I _D = -3A
Gate-Source Charge ²	Q _{gs}	-	2.0	-		
Gate-Drain Charge ²	Q _{gd}	-	2.2	-		
Turn-on Delay Time ²	T _{d(on)}	-	8.7	-	nS	V _{DS} = -15V V _{GS} = -10V I _D = -1A R _G =6Ω
Rise Time ²	T _r	-	35.9	-		
Turn-off Delay Time ²	T _{d(off)}	-	23	-		
Fall Time ²	T _f	-	8.5	-		
Input Capacitance	C _{iss}	-	545	-	pF	V _{GS} =0V V _{DS} = -15V f=1MHz
Output Capacitance	C _{oss}	-	62.5	-		
Reverse Transfer Capacitance	C _{rss}	-	48	-		
Source-Drain Diode						
Diode Forward Voltage ¹	V _{SD}	-	-	-1.2	V	I _S = -1A, V _{GS} =0V
Continuous Current	I _S	-	-	-4.1	A	V _D =V _G =0V, Force Current

Notes:

1. The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
2. Independent of operating temperature.
3. Pulsed width limited by maximum junction temperature

CHARACTERISTIC CURVES

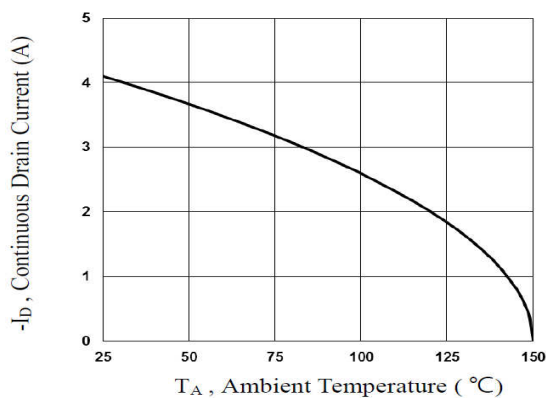


Fig.1 Continuous Drain Current vs. T_A

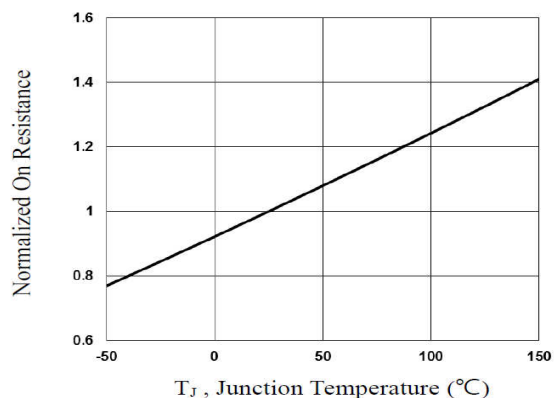


Fig.2 Normalized $R_{DS(on)}$ vs. T_J

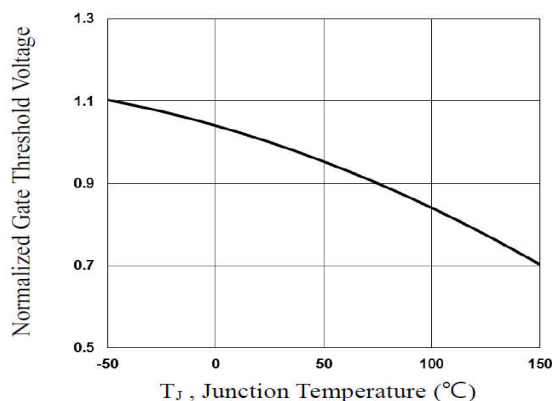


Fig.3 Normalized V_{th} vs. T_J

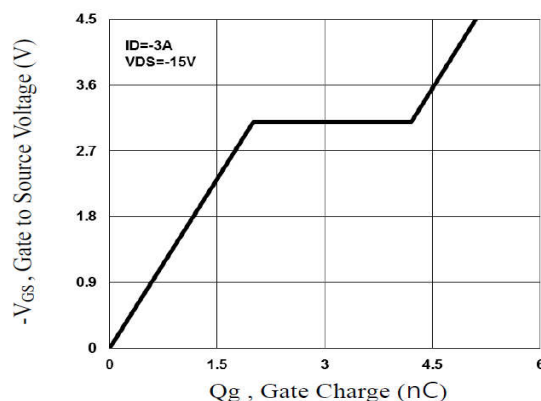


Fig.4 Gate Charge Waveform

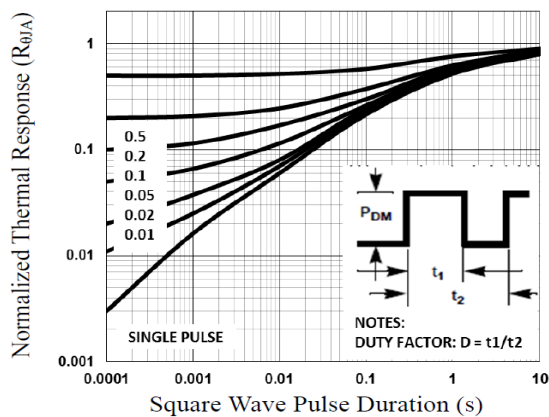


Fig.5 Normalized Transient Impedance

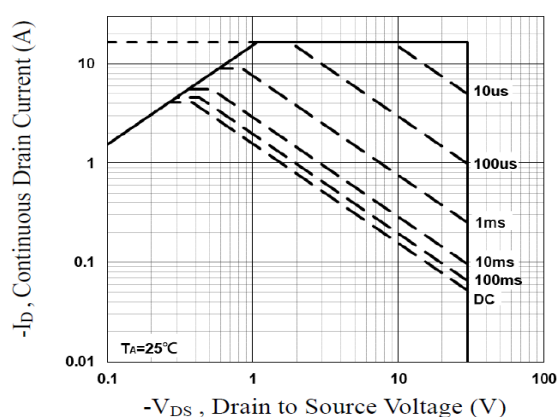


Fig.6 Maximum Safe Operation Area

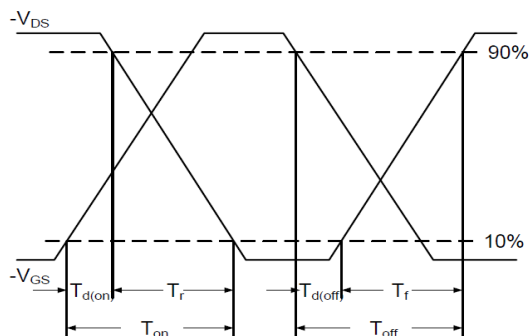


Fig.7 Switching Time Waveform

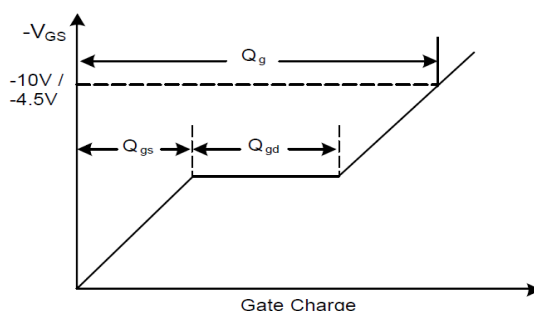
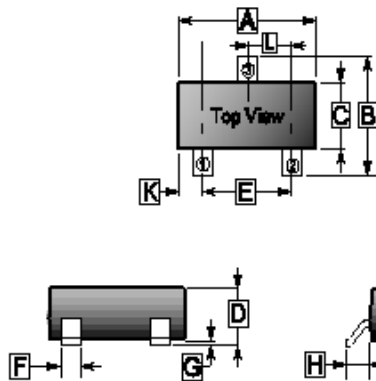


Fig.8 Gate Charge Waveform

PACKAGE OUTLINE DIMENSIONS

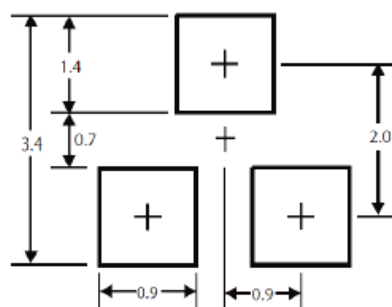
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REF.	Millimeter	
	Min.	Max.
A	2.65	3.10
B	2.10	3.00
C	1.10	1.80
D	0.89	1.45
E	1.70	2.30
F	0.28	0.55
G	-	0.18
H	0.55 REF.	
J	0.05	0.26
K	0.60 REF.	
L	0.95 TYP.	

MOUNTING PAD LAYOUT

SOT-23



*Dimensions in millimeters