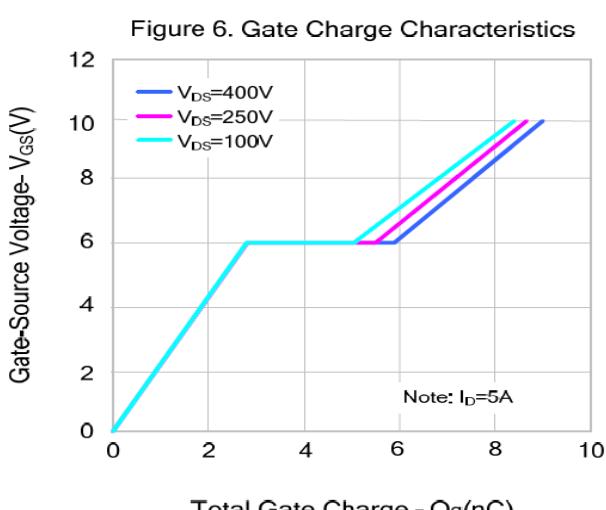
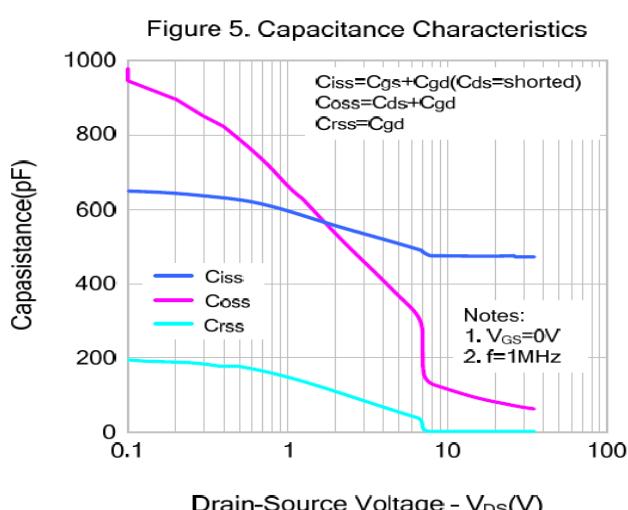
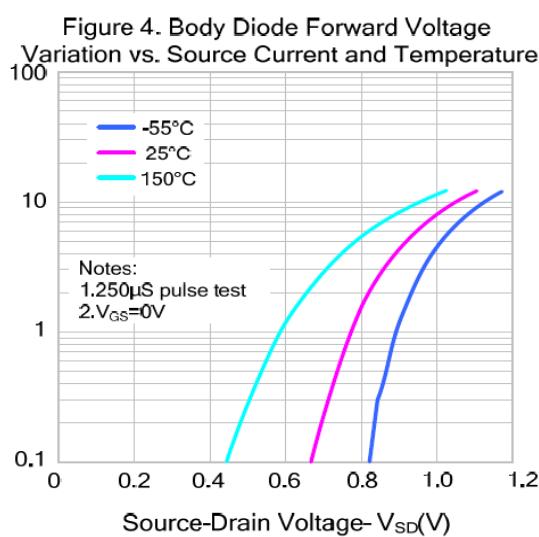
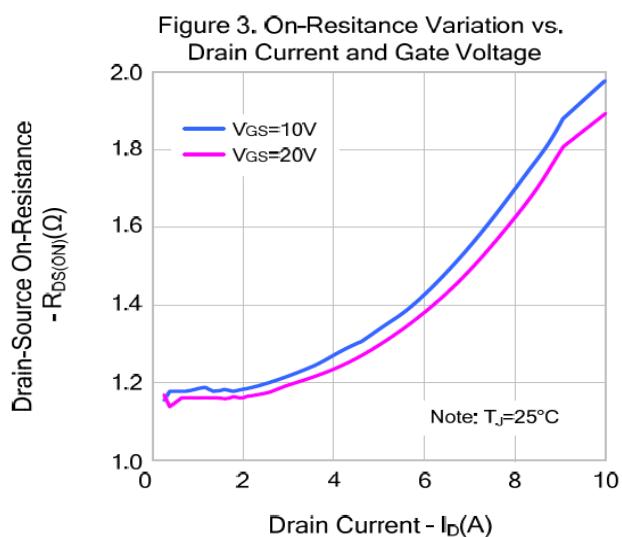
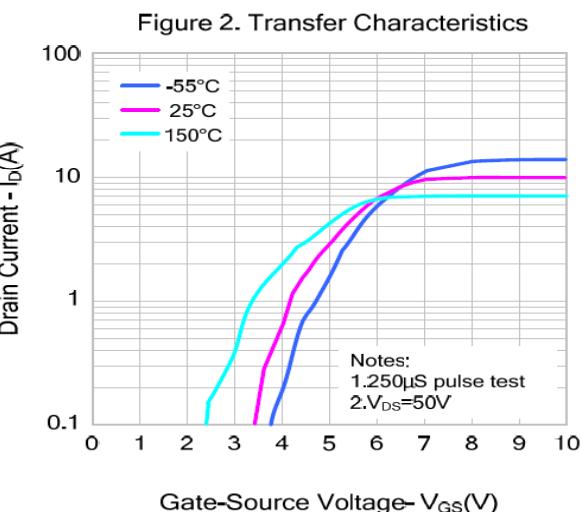
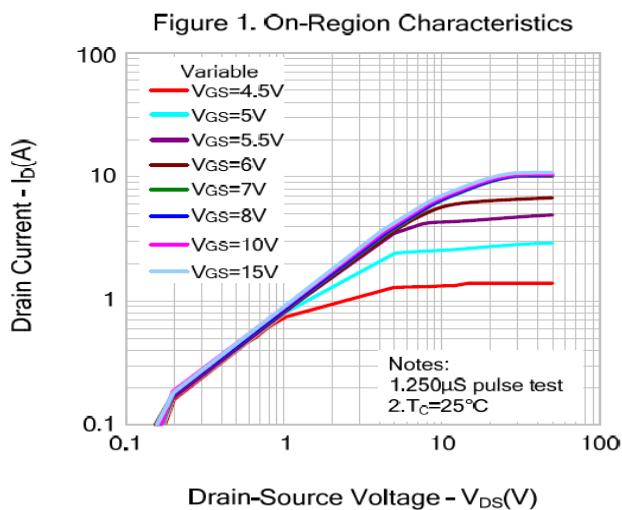


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Figure 7. Breakdown Voltage Variation vs. Temperature

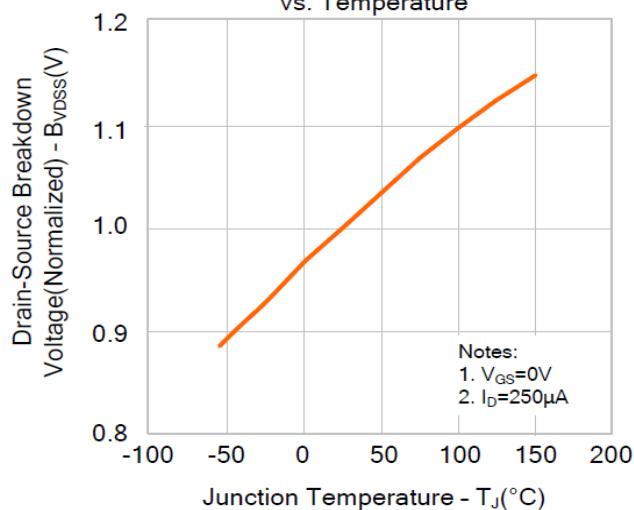


Figure 8. On-resistance Variation vs. Temperature

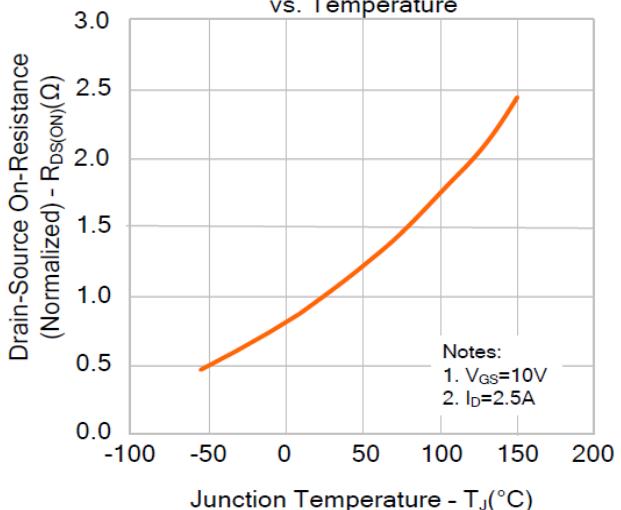


Figure 9 Max. Safe Operating Area

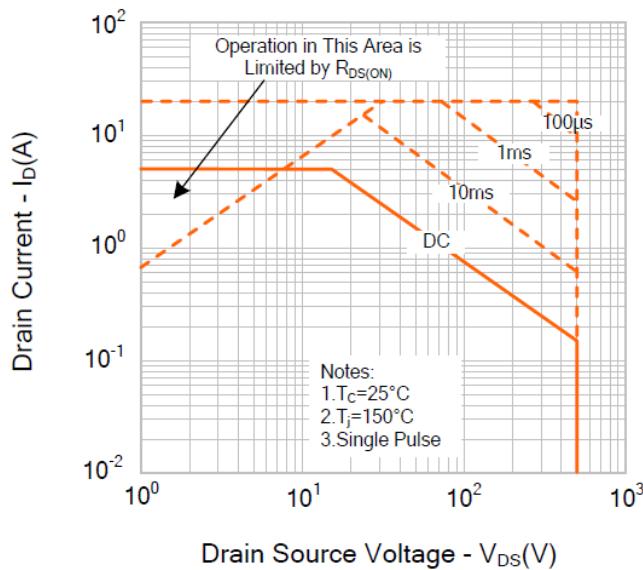
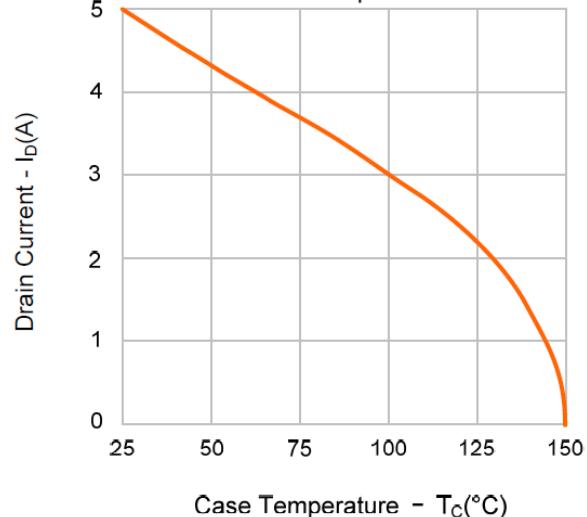
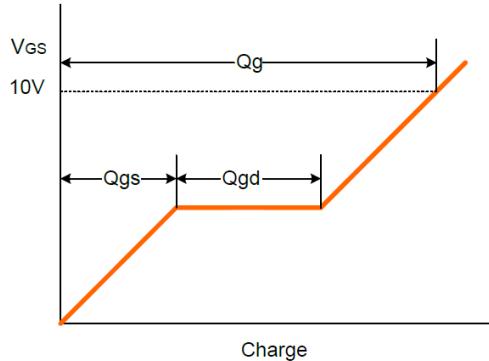
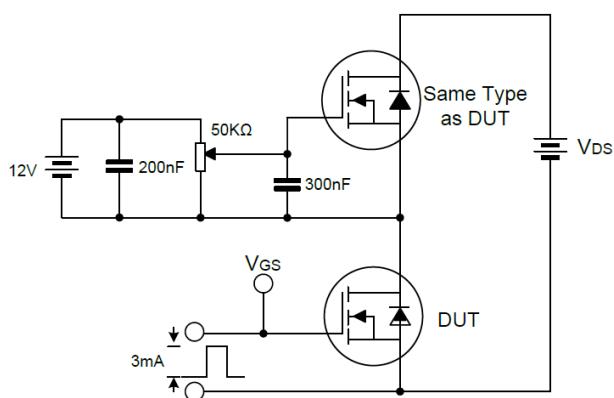


Figure 10. Maximum Drain Current vs. Case Temperature

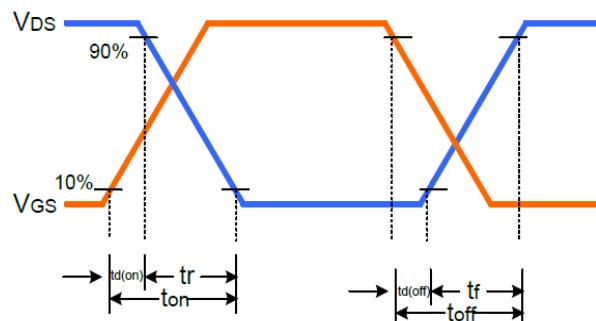
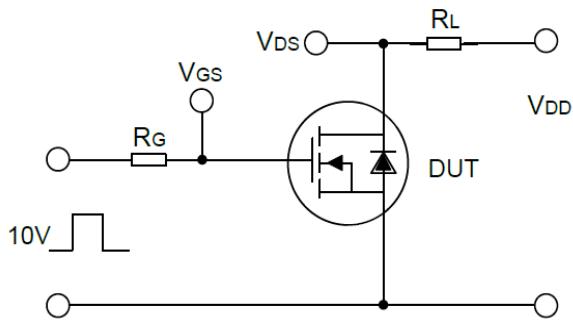


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Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching Test Circuit & Waveform

