

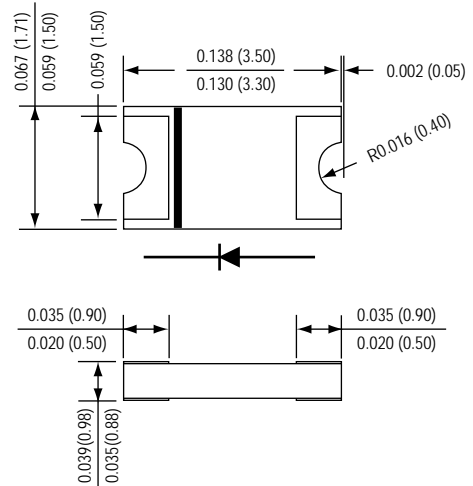
A suffix of "-C" indicates halogen-free & RoHS Compliant



**1206-S**

**FEATURES**

- \*  RoHS Compliant Product
- \*  Leadless chip form , no lead damage
- \*  Lead-free solder Joint , No Wire bond & Lead Frame
- \*  Low profile package
- \*  For surface mounted applications
- \*  Built-in strain relief
- \*  Low power loss , High efficiency
- \*  High current capability
- \*  High surge capacity
- \*  Plastic package has Underwriters Laboratory Flammability
- Classification 94V-0



Dimensions in inches and (millimeters)

**MECHANICAL DATA**

**Case :** Packed with FRP substrate and epoxy underfilled

**Terminals :** Pure Tin plated (Lead-Free),

solderable per MIL-STD-750, Method 2026.

**Polarity :** Laser marking

**Weight :** 0.012 gram

**MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS**

<i>Ratings at 25 °C ambient temperature unless otherwise specified.</i>	SYMBOLS	SCD0803	SCD0804	SCD0805	SCD0806	UNITS
Maximum repetitive peak reverse voltage	VRRM	200	400	600	800	Volts
Maximum RMS voltage	VRMS	140	280	420	560	Volts
Maximum DC blocking voltage	VDC	200	400	600	800	Volts
Maximum average forward rectified current (SEE FIG.1)	I (AV)	0.8				Amps
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load	IFSM	15				Amps
Maximum instantaneous forward voltage at 0.8 A	VF	0.96	1.30	1.70	2.50	Volts
Maximum DC reverse current at rated DC blocking voltage	IR	5 50				uA
		T <sub>A</sub> =25 °C T <sub>A</sub> =125 °C				
Maximum reverse recovery time (NOTE 1)	trr	35				nS
Typical junction capacitance (NOTE 2)	C <sub>J</sub>	10				pF
Operating junction and storage temperature range	T <sub>J</sub> ,T <sub>STG</sub>	-65 to +175				°C

NOTES : (1) Reverse recovery test condition : IF 0.5A, IR=1.0A, Irr=0.25A  
 (2) Measured at 1.0 MHz and applied reverse voltage of 4.0 Volts

**Characteristics Curve**

FIG.1 - FORWARD CURRENT DERATING CURVE

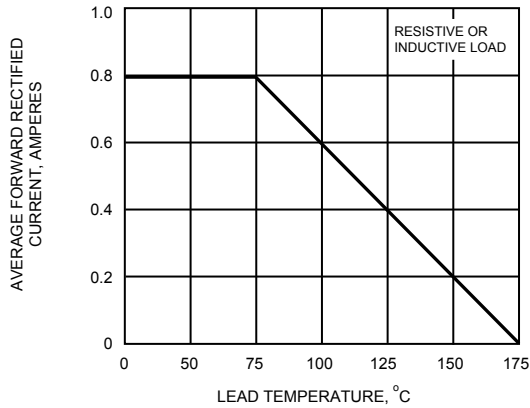


FIG.2 - MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

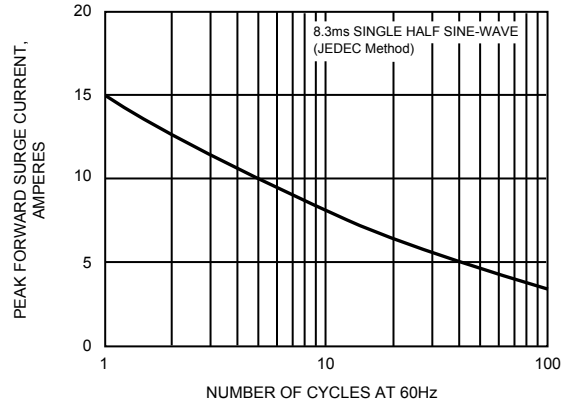


FIG.3 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

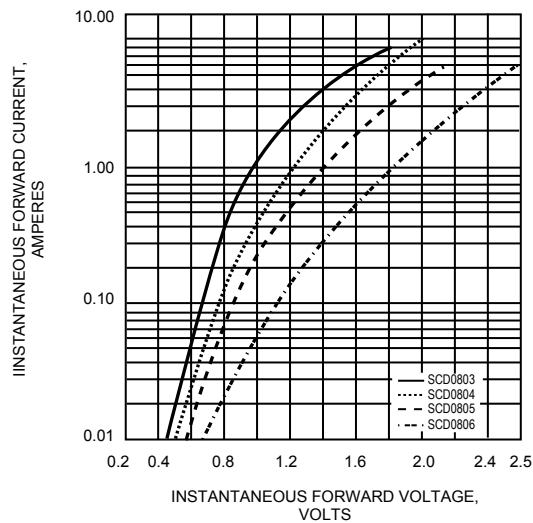


FIG.4 - TYPICAL REVERSE CHARACTERISTICS

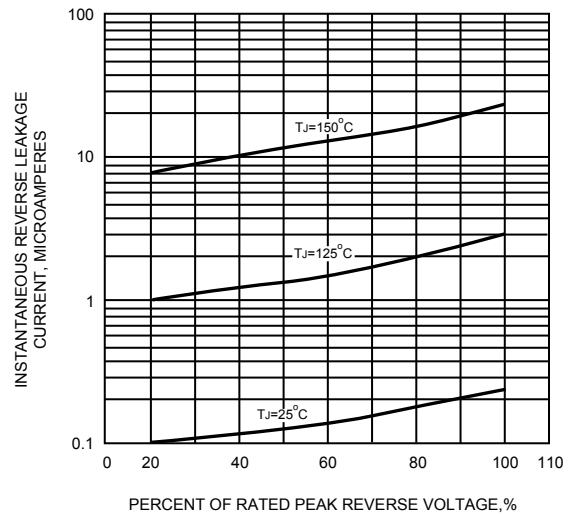


FIG.5 - TYPICAL JUNCTION CAPACITANCE

